



Custom LCD TFT Datasheet

Rev.1.1

2020 - 05 - 07

ITEM	CONTENTS	UNIT
LCD Type	TFT/Transmissive/Normally white	/
Size	4.3	Inch
Viewing Direction	all viewing angle	O' Clock
Display Mode	Normally Black	O' Clock
LCM (W × H × D)	113.50 × 76.20 × 8.43	mm ³
Active Area (W × H)	95.04 × 53.86	mm ²
Dot Pitch (W × H)	0.066 × 0.198	mm ²
Number Of Dots	480 x (RGB) × 272	/
Driver IC	FT801	/
Backlight Type	10 LEDs	/
Surface Luminance	410	cd/m ²
Interface Type	SPI/I2C	/
Color Depth	262k	/
Pixel Arrangement	RGB Vertical Stripe	/
Surface Treatment	Clear	/
Input Voltage	3.3	V
With/Without TSP	Projected Capacitive Touch Panel	/
Weight	TBD	g

Note 1: RoHS compliant

Note 2: LCM weight tolerance: ± 5%.

REVISION RECORD

REVNO.	REVDATE	CONTENTS	REMARKS
1.0	2020-05-07	Initial Release	

CONTENTS

REVISION RECORD.....	2
CONTENTS	2
1 MODULE CLASSIFICATION INFORMATION	3
2 MODULE DRAWING	4
3 ABSOLUTE MAXIMUM RATINGS.....	5
4 ELECTRICAL CHARACTERISTICS.....	5
5 BACKLIGHT CHARACTERISTICS	5
6 ELECTRO-OPTICAL CHARACTERISTICS	5
7 INTERFACE DESCRIPTION	7
8 FT801 CONTROLLER SPECIFICATIONS	8
8.1 Serial host interface.....	8
8.2 Block Diagram.....	9
8.3 Host interface SPI mode 0	9
8.4 Backlight driver block diagram	9
9 LCD TIMING CHARACTERISTICS	10
9.1 Clock and data input time diagram	10
9.2 Parallel RGB input timing table	10
10 CAPACITIVE TOUCH SCREEN PANEL SPECIFICATIONS	11
10.1 Mechanical characteristics	11
10.2 Electrical characteristics	11
11 RELIABILITY TEST	12

1 MODULE CLASSIFICATION INFORMATION

NMAS-43-CTP-FT801

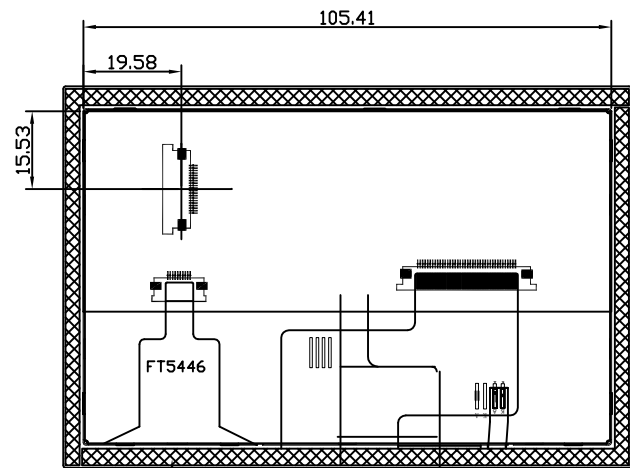
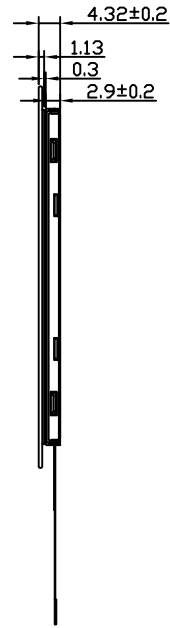
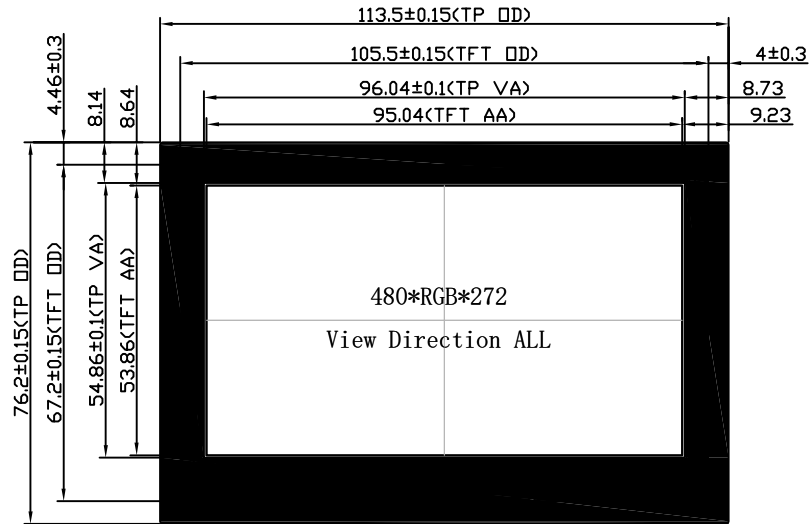
NMAM	NM our Brand part start AM Assembly product
43	4.3 inch 480x272 resolution tft lcd
CTP	Module with CTP, using FT5436 driver IC
FT801	Controller board using FTDI FT801 graphic processor

RevNo	Revision note	Date	Signature	Checked
1				
2				
3				

Top View

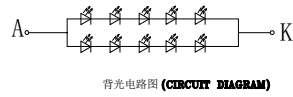
Side View

Bottom View



1	VDD3.3V
2	GND
3	SCLK/SCL
4	MISO/SDA
5	MOSI/SAO
6	CS/SAI
7	INT
8	PD
9	MODE
10	AUDIO-OUT
11	NC
12	NC
13	NC
14	NC
15	NC
16	NC
17	BLVDD5V
18	BLVDD5V
19	GND
20	GND

- NOTES:
1. Display:HSD 4.3 IPS;
 2. Driver IC:SC7283;
 3. STORAGE TEMPERATURE:-30° C TO 80° C;
OPERATING TEMPERATURE:-20° C TO 70° C;
 4. BL LED:5 CHIP-WHITE LEDS SERIES*2, If=40mA;
LCM Brightness:500cd/m2 (MIN);
CHROMATICITY COORDINATE:
X:0.30±0.03, Y:0.325±0.03;
 5. CONNECTOR TYPE :
 6. "*" CRITICAL DIMENSION; () REFERENCE DIMENSION;
 7. RECOMMENDED CELLPHONE WINDOW SIZE:0.4mm FROM LCD
 8. 偏光片上砂下光;
 9. 符合一级环境禁用管理物质



- Note:
1. COVER Glass+ITO FILM
COVER Glass: 0.7mm
ITO Glass:0.425mm
Thickness: 1.125±0.1mm
Drive IC:FT5446
 2. Operating Voltage: 2.8V-3.3 V
 3. Transmittance: ≥86%
 4. Hardness: 6H
 5. Operating TEMP: -20℃~+70℃, ≤90%RH
 6. Storage TEMP: -30℃~+80℃, ≤90%RH
 7. General Tolerance:±0.2mm

Limits Unless Otherwise Noted				设计	签名	日期	材料	PANASYS				
Tolerance Grade				绘图			单位	深圳市云田海实业有限公司				
Grade	○			审核			比例	mm	客户图号			
0 - 150	±0.10	±0.15	±0.20	批准			1:1		工程图号	NMPCB-43M2		
150+	±0.20	±0.25	±0.30	客户确认			日期		投影法		版本	1.0

3 ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN	MAX	UNIT
Supply Voltage For Logic	VDD	-0.3	3.6	V
Supply Voltage For CTP Logic	VDD-VSS	-0.5	3.6	V
Input Voltage For Logic	VIN	-0.3	VDD	V
LED forward current (each LED)	IF	-	30	mA
Operating Temperature	T _{OP}	-20	70	°C
Storage Temperature	T _{ST}	-30	80	°C
Humidity	RH	-	90% (Max 60°C)	RH

4 ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	NOTES
Supply Voltage For Module	VDD	3.0	3.3	3.6	V	
Input Voltage for LED Inverter	BLVDD	2.8	3.3	5.5	V	
Input Current (Exclude LED Backlight)	IDD	-	70	87	mA	VDD = 3.3V
LED Backlight Current	IDD _{backlight}		260	325	mA	BLVDD=3.3V
LED Backlight Current	IDD _{backlight}		150	187	mA	BLVDD=5V
Total Input Current (Include LED Backlight 100%)	IDD _{total}	-	330	412	mA	BLVDD=3.3V
Input Voltage for LED Inverter	BLVDD	2.8	3.3	5.5	V	
Input Voltage 'H' level	V _{IH}	0.7VDD	-	VDD	V	
Input Voltage 'L' level	V _{IL}	VSS	-	0.2VDD	V	

Note: The LED life time is defined as the module brightness decrease to 50% original brightness at Ta=25°C.

5 BACKLIGHT CHARACTERISTICS

ITEM	SYMBOL	MIN	TYP	MAX	UNIT
Voltage for LED backlight	V _I	15.0	16.0	17.0	V
Current for LED backlight	I _I	-	40	-	mA
LED Life Time	-	30000	-	-	Hrs

Note: The LED life time is defined as the module brightness decrease to 50% original brightness at Ta=25°C.

6 ELECTRO-OPTICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	REMARK	NOTE
Response Time	Tr+Tf	$\theta=0^\circ$ $\phi=0^\circ$ Ta=25	-	25	50	ms	Figure 1	4
Contrast Ratio	Cr		400	500	-	---	Figure 2	1
Luminance Uniformity	δ WHITE		75	80	-	%	Figure 2	3
Surface Luminance	Lv		-	410	-	cd/m ²	Figure 2	2
Viewing Angle Range	θ	$\phi = 90^\circ$	-	80	-	deg	Figure 3	6
		$\phi = 270^\circ$	-	80	-	deg	Figure 3	
		$\phi = 0^\circ$	-	80	-	deg	Figure 3	
		$\phi = 180^\circ$	-	80	-	deg	Figure 3	
CIE (x, y) Chromaticity	Red	$\theta=0^\circ$ $\phi=0^\circ$ Ta=25	x	-	0.581	-	Figure 2	5
			y	-	0.345	-		
	Green		x	-	0.348	-		
			y	-	0.581	-		
	Blue		x	-	0.153	-		
			y	-	0.095	-		
	White		x	-	0.315	-		
			y	-	0.335	-		

Note 1. Contrast Ratio(CR) is defined mathematically as below, for more information see Figure 1.

$$\text{Contrast Ratio} = \frac{\text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)}}$$

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see Figure 2.

L_v = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)

Note 3. The uniformity in surface luminance δ WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the maximum luminance of 5 points luminance by minimum luminance of 5 points luminance. For more information see Figure 2.

$$\delta \text{ WHITE} = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}$$

Note 4. Response time is the time required for the display to transition from white to black (Rise Time, T_r) and from black to white (Decay Time, T_f). For additional information see FIG 1. The test equipment is Autronic-Melchers's ConoScope series.

Note 5. CIE (x, y) chromaticity, the x, y value is determined by measuring luminance at each test position 1 through 5, and then make average value.

Note 6. Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see Figure 3.

Note 7. For viewing angle and response time testing, the testing data is based on Autronic-Melchers's ConoScope series. Instruments for Contrast Ratio, Surface Luminance, Luminance Uniformity, CIE the test data is based on TOPCON's BM-5 photo detector.

Figure 1. The definition of response time

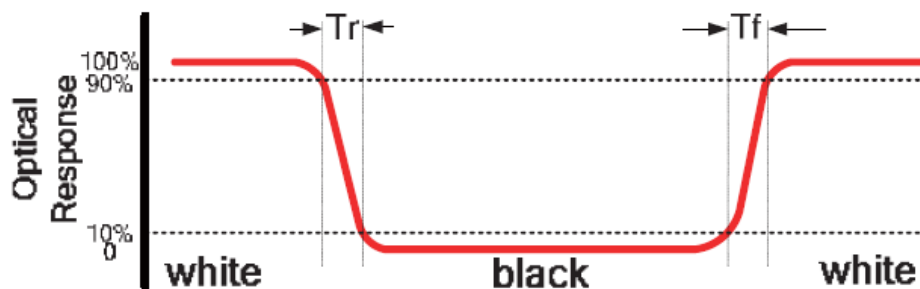


Figure 2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x,y) chromaticity

A : 5 mm
 B : 5 mm
 H, V : Active Area
 Light spot size $\varnothing=5\text{mm}$, 500mm distance from the LCD surface to detector lens
 measurement instrument is TOPCON's luminance meter BM-5

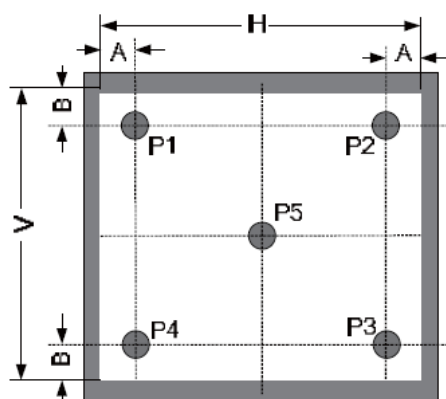
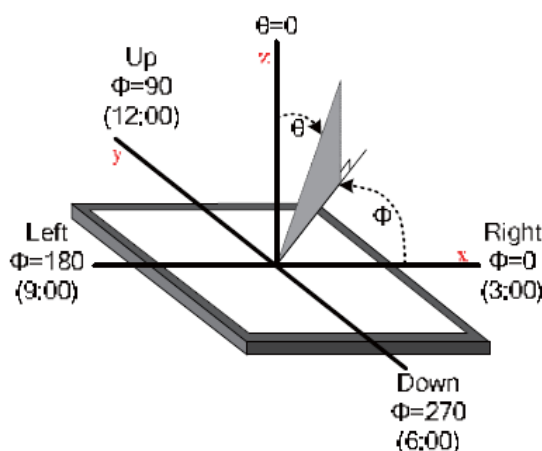


Figure 3. The definition of viewing angle



7 INTERFACE DESCRIPTION

PIN NO.	SYMBOL	DESCRIPTION
1	VDD	Power Supply
2	GND	Ground
3	SPI_SCLK/ I2C_SCL	SPI SCK Signal / I2C SCL Signal, Internally 47k Pull UP
4	MISO/ I2C_SDA	SPI MISO Signal / I2C SDA Signal, Internally 47k Pull UP
5	MOSI/ I2C_SA0	SPI MOSI Signal / I2C Slave Address Bit 0, Internally 47k Pull UP
6	CS/I2C_SA1	SPI Chip Select Signal / I2C Slave Address Bit 1, Internally 47k Pull UP
7	INT	Interrupt Signal, Active Low, Internally 47k Pull UP
8	PD	Power Down Signal, Active Low, Internally 47k Pull UP
9	MODE	Host Interface SPI(Pull Low) or I2C(Pull Up) Mode Select Input, Internally 10k Pull DOWN
10	AUDIO_OUT	Audio Out Signal
11	NC	Not Connected
12	NC	Not Connected
13	NC	Not Connected
14	NC	Not Connected
15	NC	Not Connected
16	NC	Not Connected
17	BLVDD	Backlight Power Supply, Can Be Connected to VDD
18	BLVDD	Backlight Power Supply, Can Be Connected to VDD
19	BLGND	Backlight Ground, Internally connected to GND
20	BLGND	Backlight Ground, Internally connected to GND

8 FT801 CONTROLLER SPECIFICATIONS

FT801 or EVE (Embedded Video Engine) simplifies the system architecture for advanced human machine interfaces (HMIs) by providing functionality for display, audio, and touch as well as an object oriented architecture approach that extends from display creation to the rendering of the graphics.

8.1 Serial host interface

Figure 4. SPI interface connection

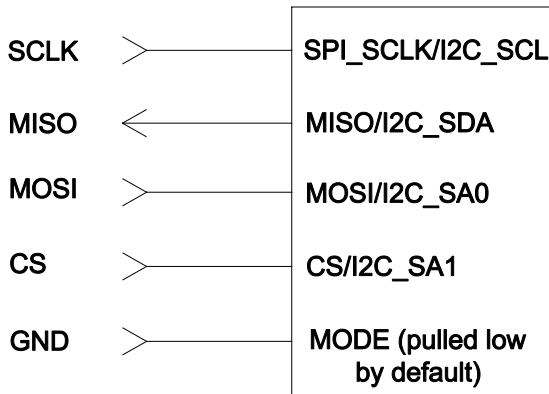
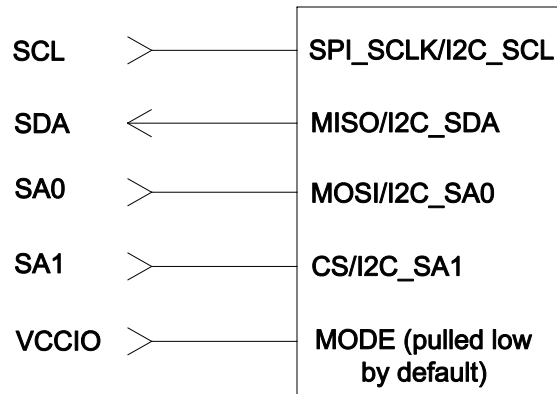


Figure 5. I2C interface connection



SPI Interface – the SPI slave interface operates up to 30MHz.

Only SPI mode 0 is supported. The SPI interface is selected by default (MODE pin is internally pulled low by 47k resistor).

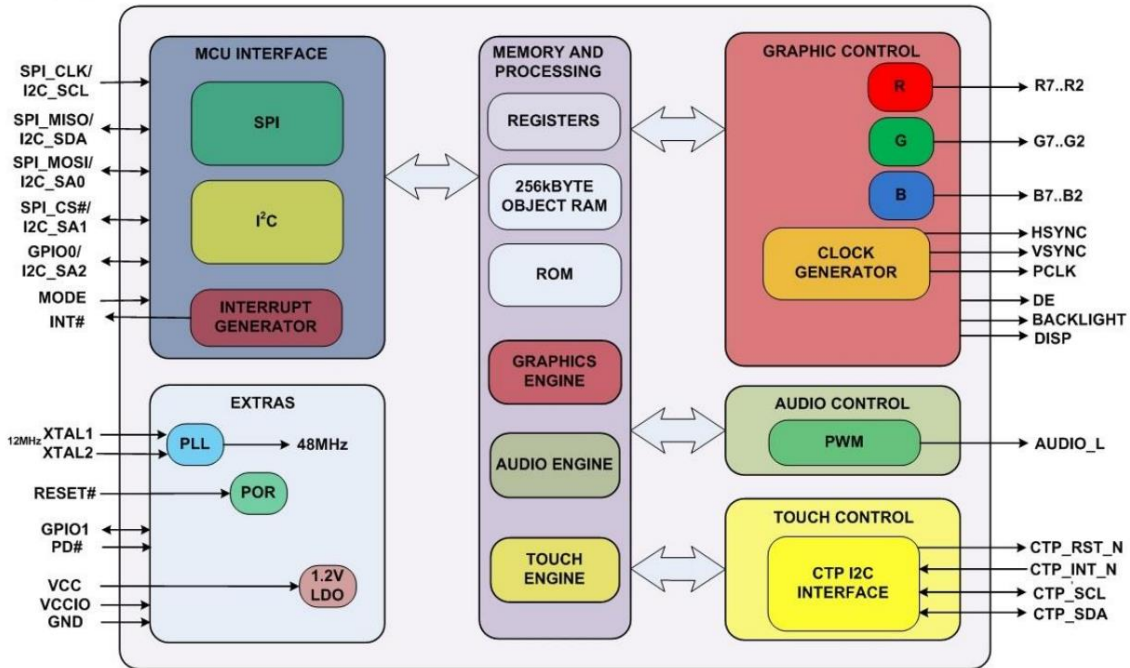
I²C Interface – the I²C slave interface operates up to 3.4MHz, supporting standard-mode, fast-mode, fast-mode plus and high-speed mode.

The I²C device address is configurable between 20h to 23h depending on the I²C_SA[1:0] pin setting, i.e. the 7-bit I²C slave address is 0b'01000A1A0.

The I²C interface is selected when the MODE pin is tied to VDDIO.

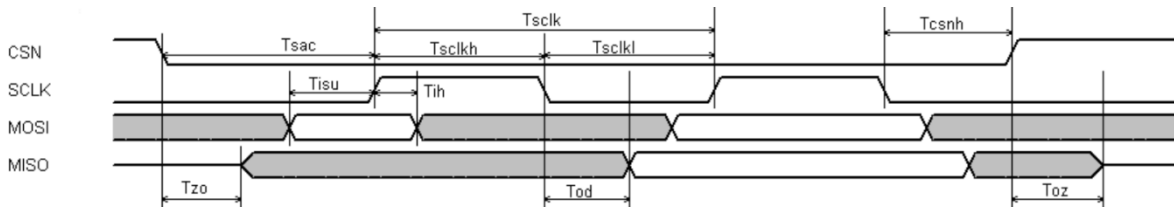
8.2 Block Diagram

Figure 6. FT801 Block diagram



8.3 Host interface SPI mode 0

Figure 7. SPI timing diagram



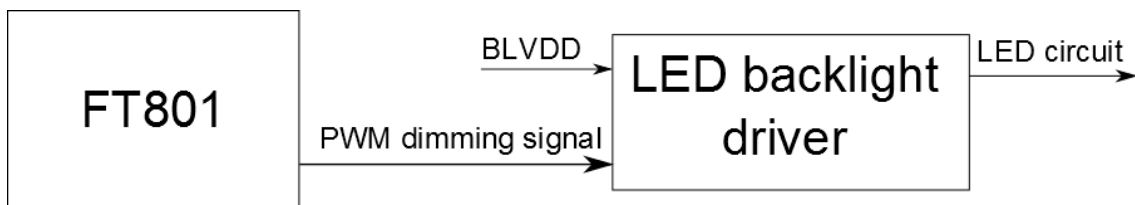
For more information about FT801 controller please go to official FT801 Datasheet.

http://www.ftdichip.com/Support/Documents/DataSheets/ICs/DS_FT801.pdf

8.4 Backlight driver block diagram

Backlight enable signal is internally connected to FT801 Backlight control pin. This pin is controlled by two FT801's registers. One of them specifies the PWM output frequency, second one specifies the duty cycle. Refer to FT801 datasheet for more information.

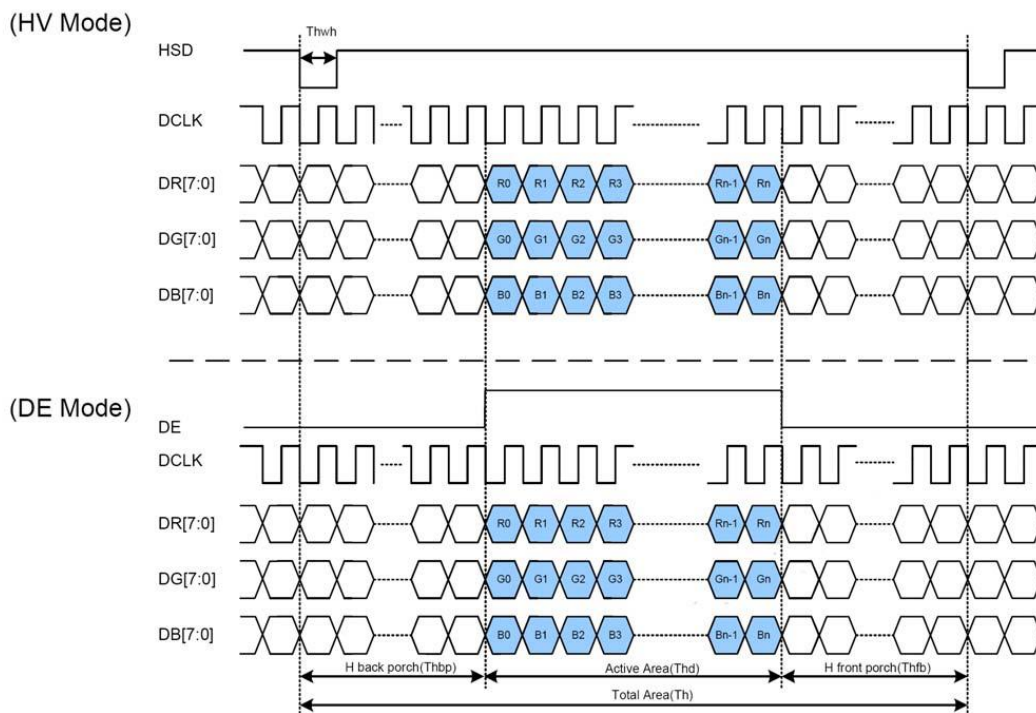
Figure 8. Backlight driver block diagram



9 LCD TIMING CHARACTERISTICS

9.1 Clock and data input time diagram

Figure 9. Clock and data input time diagram



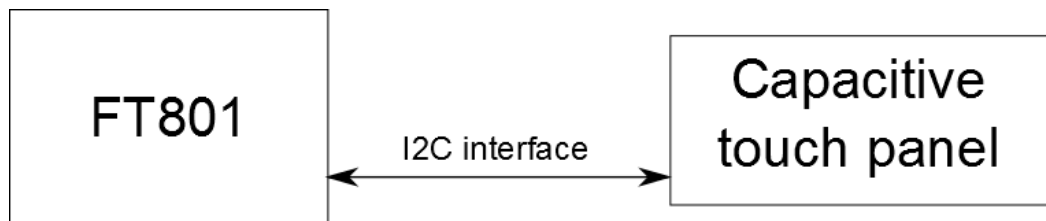
9.2 Parallel RGB input timing table

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
DCLK Frequency	Fclk	5	9	12	MZH
DCLK Period	Tclk	83	110	220	ns
VSD Period Time	Tv	275	288	335	H
VSD Display Area	Tvd		272		H
VSD Back Porch	Tvb	2	12		H
VSD Front Porch	Tvfp	1	4		H
HSD Period Time	Th	490	531	605	DCLK
HSD Display Area	Thd		480		DCLK
HSD Back Porch	Thbp	8	43		DCLK
HSD Front Porch	Thfp	2	8		DCLK

10 CAPACITIVE TOUCH SCREEN PANEL SPECIFICATIONS

Capacitive Touch Panel is directly connected to FT801 module. Therefore communication with Capacitive touch panel is simplified to read registers of FT801.

Figure 10. Capacitive Touch Panel Connection



10.1 Mechanical characteristics

DESCRIPTION	INL SPECIFICATION	REMARK
Touch Panel Size	4.3 inch	
Outline Dimension (OD)	113.5mm x 76.2mm	Cover Lens Outline
Product Thickness	1.7mm	
Glass Thickness	0.7mm	
Ink View Area	97.0mm x 55.5mm	
Sensor Active Area	98.0mm x 56.5mm	
Input Method	5 Finger	
Activation Force	Touch	
Surface Hardness	≥6H	

10.2 Electrical characteristics

DESCRIPTION	SPECIFICATION
Operating Voltage	DC 2.8~3.3V
Power Consumption (IDD)	Active Mode
	Sleep Mode
Interface	I ² C
Linearity	<1.5%
Controller	FT5436
I2C address	0x38 (7 bit address)
Resolution	480*272

11 RELIABILITY TEST

NO.	TEST ITEM	TEST CONDITION
1	High Temperature Storage	80±2°C/240hours
2	Low Temperature Storage	-30±2°C/240hours
3	High Temperature Operating	70±2°C/240hours
4	Low Temperature Operating	-20±2°C/240hours
5	Temperature Cycle	-30±2°C~25~80±2°C × 20 cycles (30min.) (5min.) (30min.)
6	Damp Proof Test	60°C ±5°C × 90%RH/240hours
7	Vibration Test	Frequency 10Hz~55Hz Amplitude of vibration : 1.5mm Sweep: 10Hz~55Hz~10Hz X, Y, Z 2 hours for each direction.
8	Package Drop Test	Height:60 cm 1 corner,3 edges,6 surfaces
9	ESD Test	Air: ±4KV 150pF/330Ω 5 times Contact: ±2KV 150pF/330Ω 5 time