

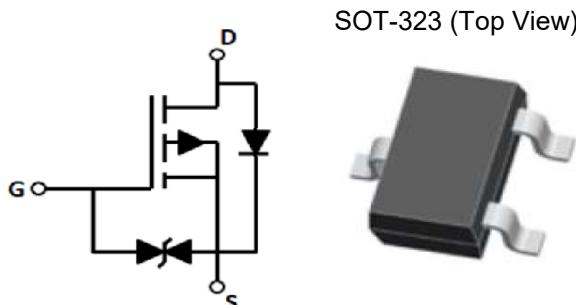
Description

CM3139E is the P-Channel enhancement mode power field effect transistors with high cell density, trench technology. This high density process and design have been optimized switching performance and especially tailored to minimize on-state resistance.

Features

- V_{DS}: -20V
- I_D: -0.62A
- R_{DSON} (@V_{GS}=-4.5V) : < 520mΩ
- R_{DSON} (@V_{GS}=-2.5V) : < 750mΩ
- High density cell design for extremely low R_{DSON}
- Excellent on-resistance and DC current capability

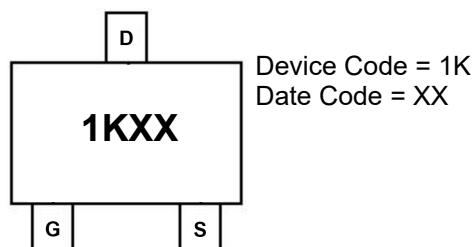
Equivalent Circuit and Pin Configuration



Applications

- Cellular Handsets and Accessories
- Personal Digital Assistants
- Portable Instrumentation
- Load switch

Marking Information



Ordering Information

Part Number	Packaging	Reel Size
CM3139E	3000/Tape & Reel	7 inch

Absolute Maximum Ratings (TA=25 °C unless otherwise noted)

Parameter	Symbol	Maximum	Unit
Drain-source Voltage	V _{DS}	-20	V
Gate-source Voltage	V _{GS}	±10	V
Continuous Drain Current	I _D	-0.62	A
		-0.48	A
		-0.69	A
Pulsed Drain Current ⁽¹⁾	I _{DM}	-2.5	A
Total Power Dissipation @ TA=25°C ⁽²⁾	P _D	340	mW
		430	
Thermal Resistance Junction-to-Ambient ⁽²⁾ @Steady State	R _{θJA}	366	°C/W
Thermal Resistance Junction-to-Ambient ⁽²⁾ @t≤5s		294	
Junction and Storage Temperature Range	T _{J,TSTG}	-55 to +150	°C

Electrical Characteristics (T_J=25 °C unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BVDSS	V _{GS} =0V, I _D =-250μA	-20			V
Zero Gate Voltage Drain Current	I _{DSS}	V _D =-20V, V _{GS} =0V, T _C =25°C			-1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±10V, V _D =0V			±10	uA
Gate Threshold Voltage	V _{GS(th)}	V _D =V _{GS} , I _D =-250μA	-0.35		-1.2	V
Static Drain-Source on-Resistance	R _{D(on)}	V _{GS} =-4.5V, I _D =-0.6A		360	520	mΩ
		V _{GS} =-2.5V, I _D =-0.5A		570	750	
Diode Forward Voltage	V _{SD}	I _S =-0.62A, V _{GS} =0V		-0.8	-1.2	V
Maximum Body-Diode Continuous Current	I _S				-0.62	A
Dynamic Parameters						
Input Capacitance	C _{iss}	V _D =-10V, V _{GS} =0V, f=1MHz		135		pF
Output Capacitance	C _{oss}			110		
Reverse Transfer Capacitance	C _{rss}			72		
Switching Parameters						
Total Gate Charge	Q _g	V _{GS} =-4.5V, V _D =-10V, I _D =-2.0A		3.9		nC
Gate Source Charge	Q _{gs}			0.7		
Gate Drain Charge	Q _{gd}			0.9		
Turn-on Delay Time	t _{D(on)}	V _{GS} =-4.5V, V _D =-10V, I _D =-1.0A, R _{GEN} =2.5Ω		12		ns
Turn-on Rise Time	t _r			54		
Turn-off Delay Time	t _{D(off)}			15		
Turn-off Fall Time	t _f			9		

Noted: (1) Pulse Test: Pulse Width≤300us,Duty cycle ≤2%.

(2) Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch. With 2oz Copper ,t≤10s

Typical Performance Characteristics

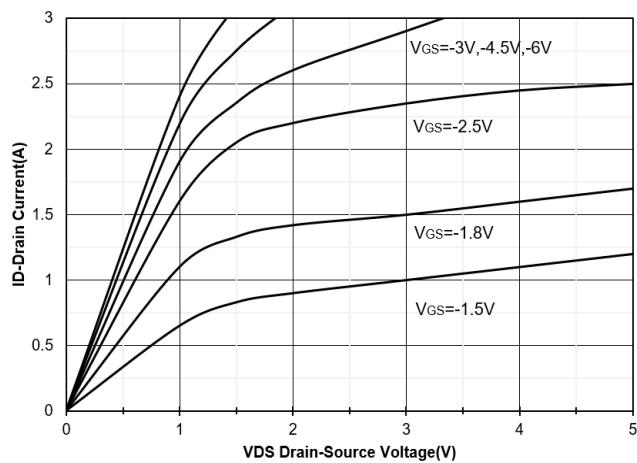


Figure 1. Output Characteristics

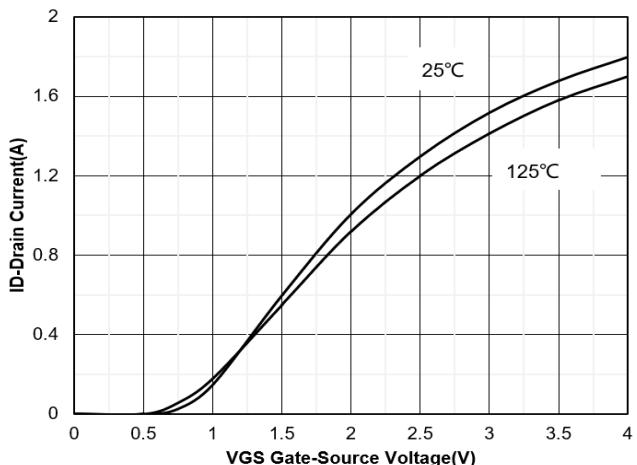


Figure 2. Transfer Characteristics

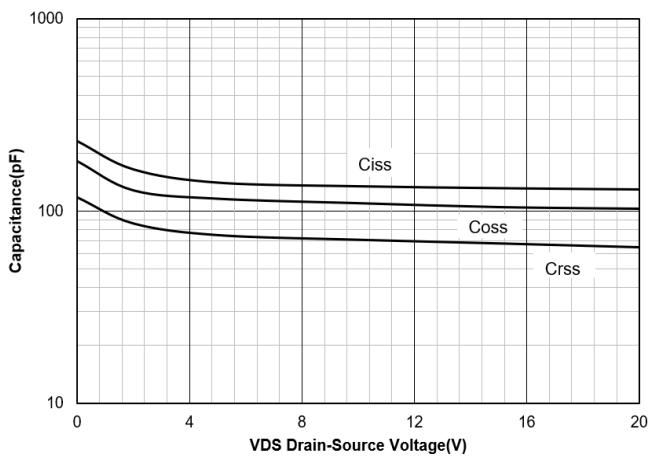


Figure 3. Capacitance Characteristics

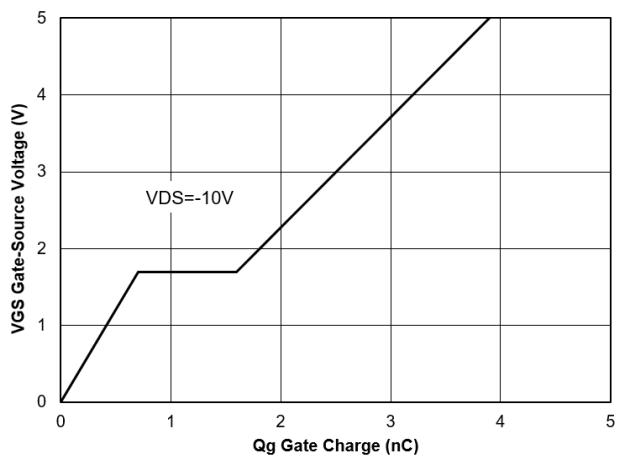


Figure 4. Gate Charge

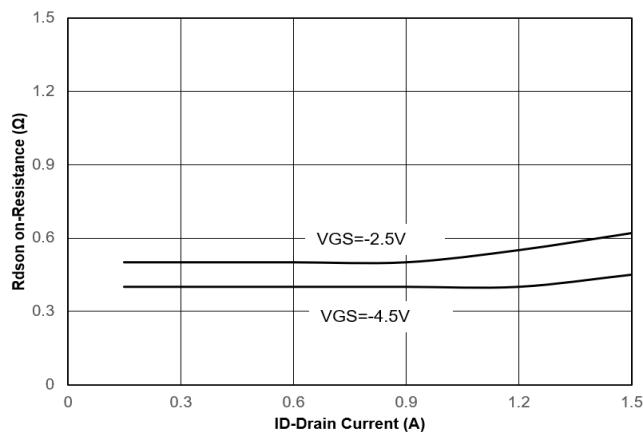


Figure 5. Drain-Source on Resistance

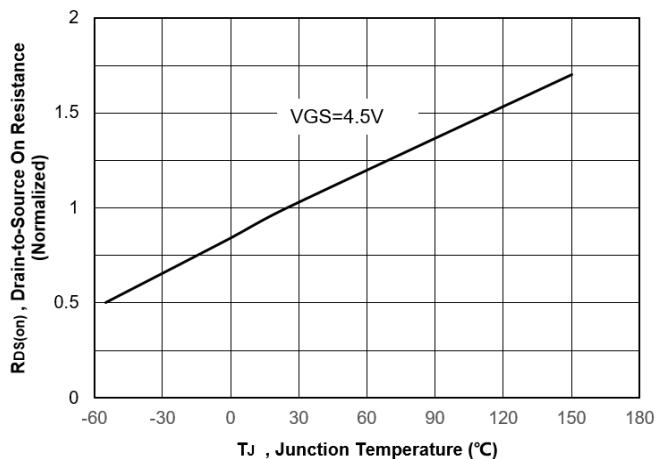


Figure 6. Normalized On-Resistance

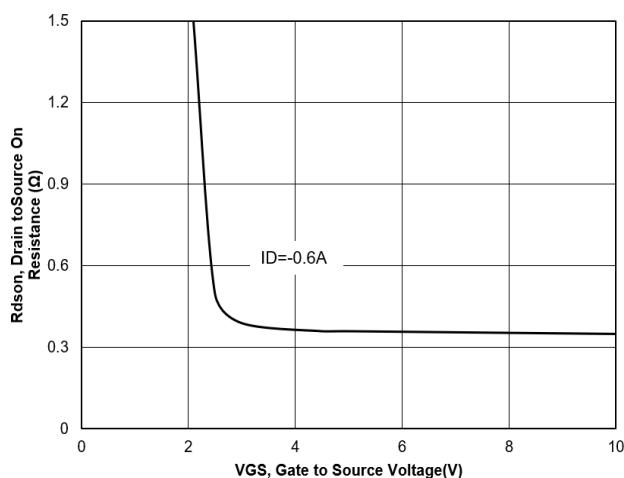


Figure 7. Typical Drain to Source ON Resistance
VS Gate Voltage and Drain Current

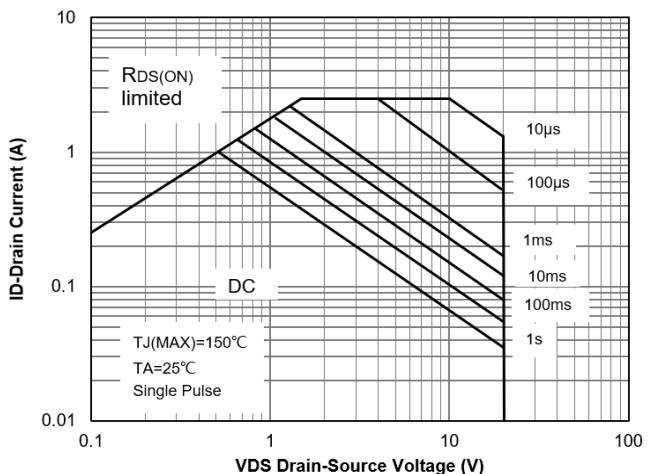


Figure 8. Safe Operation Area

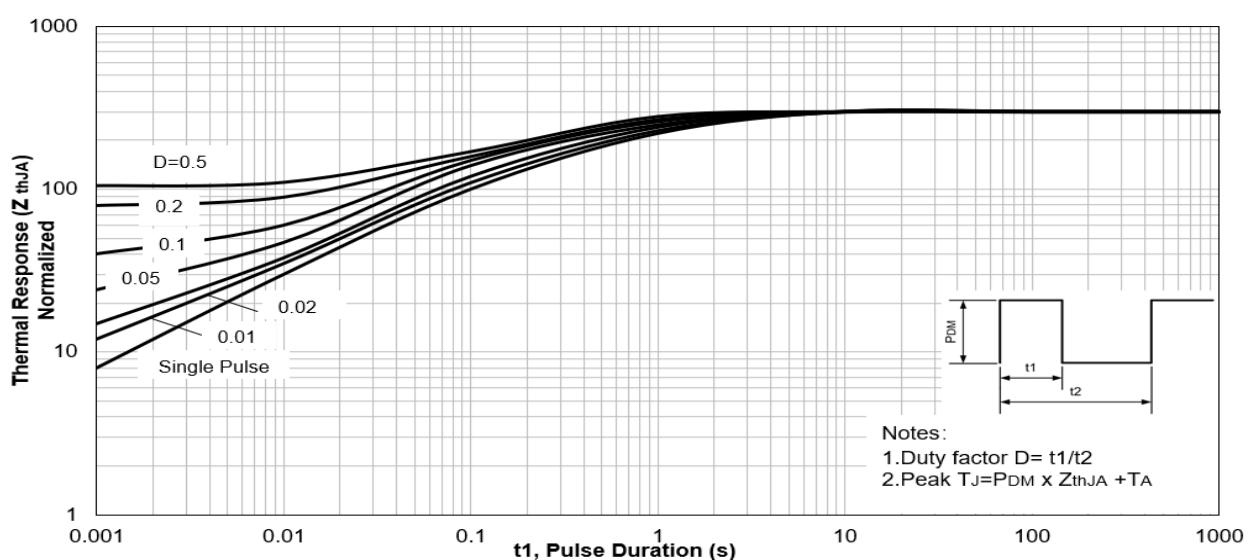


Figure 9. Maximum Effective Transient Thermal Impedance ,Junction-to-Ambient

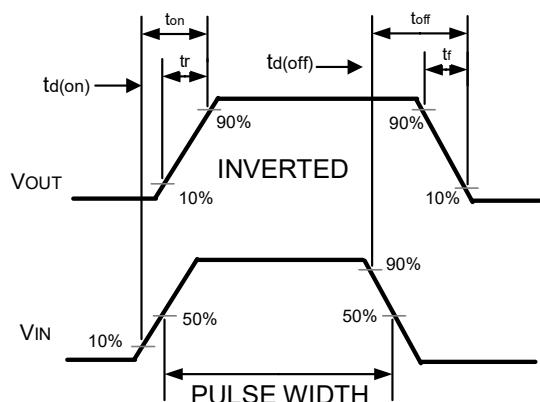
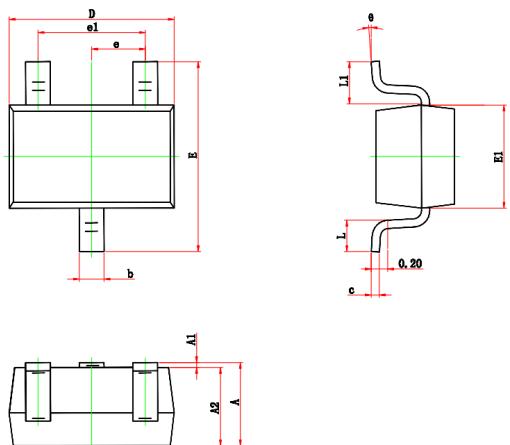


Figure 10. Switching wave

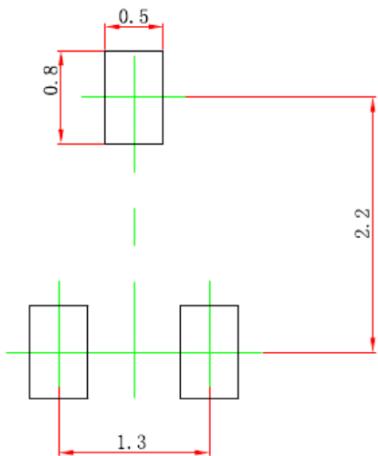
SOT-323 Package Outline Drawing

(Unit : mm)



SYM	DIMENSIONS					
	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.90	--	1.10	0.035	--	0.043
A1	0.00	--	0.10	0.000	--	0.004
A2	0.90	--	1.00	0.035	--	0.039
b	0.20	--	0.40	0.008	--	0.016
c	0.08	--	0.15	0.003	--	0.006
D	2.00	--	2.20	0.079	--	0.087
E	2.15	--	2.45	0.085	--	0.096
E1	1.15	--	1.35	0.045	--	0.053
e	0.65 TYP			0.026 TYP		
e1	1.20	--	1.40	0.047	--	0.055
L	0.26	--	0.46	0.010	--	0.018
L1	0.525 REF			0.021 REF		
Θ	0°	--	8°	0°	--	8°

Suggested Land Pattern



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