

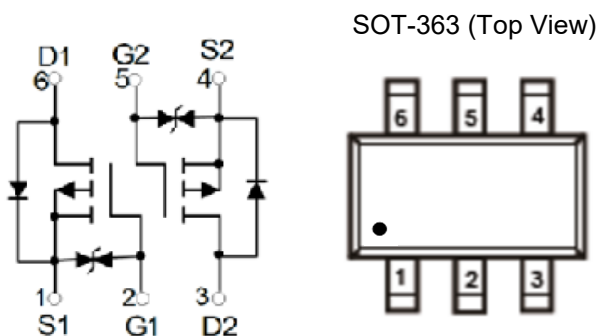
## Description

CM3139EW is the P-Channel enhancement mode power field effect transistors with high cell density, trench technology. This high density process and design have been optimized switching performance and especially tailored to minimize on-state resistance.

## Features

- $V_{DS}$ : -20V
- $I_D$ : -0.62A
- $R_{DS(on)}$  (@ $V_{GS}=-4.5V$ ): < 520m $\Omega$
- $R_{DS(on)}$  (@ $V_{GS}=-2.5V$ ): < 750m $\Omega$
- High density cell design for extremely low  $R_{DS(on)}$
- Excellent on-resistance and DC current capability

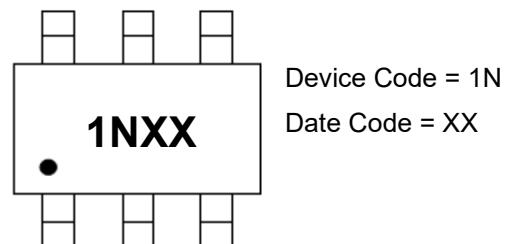
## Equivalent Circuit and Pin Configuration



## Applications

- Cellular Handsets and Accessories
- Personal Digital Assistants
- Portable Instrumentation
- Load switch

## Marking Information



## Ordering Information

Part Number	Packaging	Reel Size
CM3139EW	3000/Tape & Reel	7 inch

## Absolute Maximum Ratings (TA=25 °C unless otherwise noted)

Parameter	Symbol	Maximum	Unit	
Drain-source Voltage	$V_{DS}$	-20	V	
Gate-source Voltage	$V_{GS}$	$\pm 10$	V	
Continuous Drain Current	$I_D$	$T_A=25^\circ C$ , Steady State	-0.62	A
		$T_A=75^\circ C$ , Steady State	-0.48	A
		$T_A=25^\circ C$ , $t \leq 5s$	-0.69	A
Pulsed Drain Current <sup>(1)</sup>	$I_{DM}$	-2.5	A	
Total Power Dissipation @ $T_A=25^\circ C$ <sup>(2)</sup>	$P_D$	Steady State	340	mW
		$t \leq 5s$	430	
Thermal Resistance Junction-to-Ambient <sup>(2)</sup> @Steady State	$R_{\theta JA}$	366	$^\circ C/W$	
Thermal Resistance Junction-to-Ambient <sup>(2)</sup> @ $t \leq 5s$		294		
Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	$^\circ C$	

**Electrical Characteristics (T<sub>J</sub>=25 °C unless otherwise noted)**

Parameter	Symbol	Conditions	Min	Typ	Max	Units
<b>Static Parameter</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	-20			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-20V, V <sub>GS</sub> =0V, T <sub>C</sub> =25°C			-1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±10V, V <sub>DS</sub> =0V			±10	uA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-0.35		-1.2	V
Static Drain-Source on-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-0.6A		360	520	mΩ
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-0.5A		570	750	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-0.62A, V <sub>GS</sub> =0V		-0.8	-1.2	V
Maximum Body-Diode Continuous Current	I <sub>S</sub>				-0.62	A
<b>Dynamic Parameters</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-10V, V <sub>GS</sub> =0V, f=1MHz		66		pF
Output Capacitance	C <sub>oss</sub>			13		
Reverse Transfer Capacitance	C <sub>rss</sub>			8		
<b>Switching Parameters</b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>GS</sub> =-4.5V, V <sub>DS</sub> =-10V, I <sub>D</sub> =-2.0A		3.9		nC
Gate Source Charge	Q <sub>gs</sub>			0.7		
Gate Drain Charge	Q <sub>gd</sub>			0.9		
Turn-on Delay Time	t <sub>D(on)</sub>	V <sub>GS</sub> =-4.5V, V <sub>DD</sub> =-10V, I <sub>D</sub> =-1.0A, R <sub>GEN</sub> =2.5Ω		12		ns
Turn-on Rise Time	t <sub>r</sub>			54		
Turn-off Delay Time	t <sub>D(off)</sub>			15		
Turn-off Fall Time	t <sub>f</sub>			9		

Noted: (1) Pulse Test: Pulse Width ≤ 300us, Duty cycle ≤ 2%.

(2) Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch. With 2oz Copper, t ≤ 10s

**Typical Performance Characteristics**

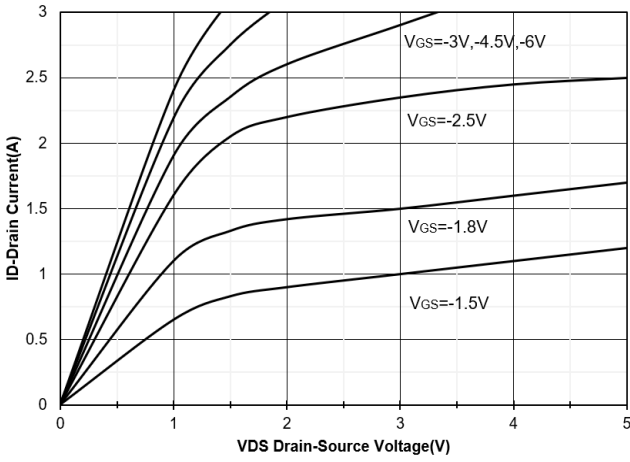


Figure 1. Output Characteristics

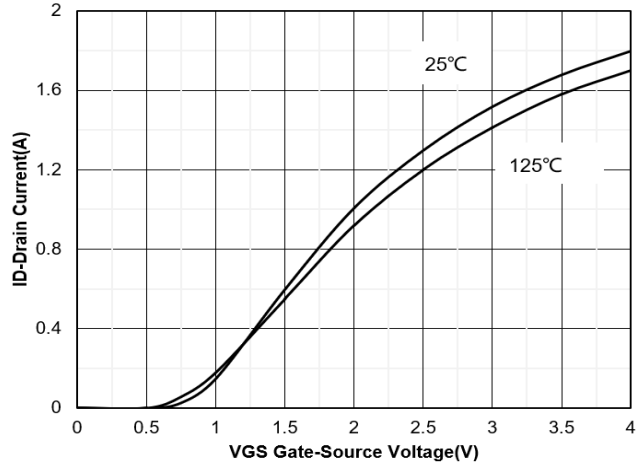


Figure 2. Transfer Characteristics

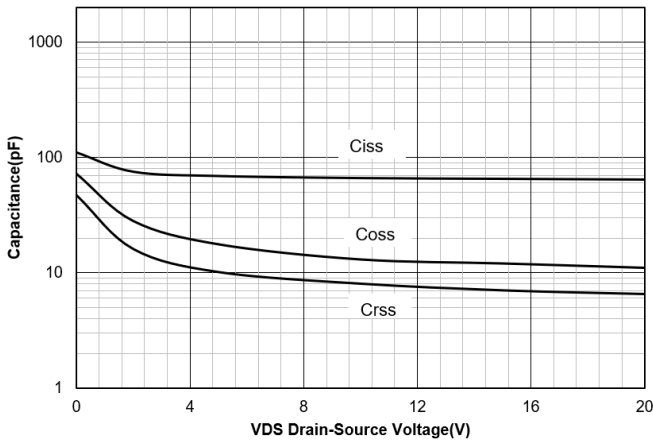


Figure 3. Capacitance Characteristics

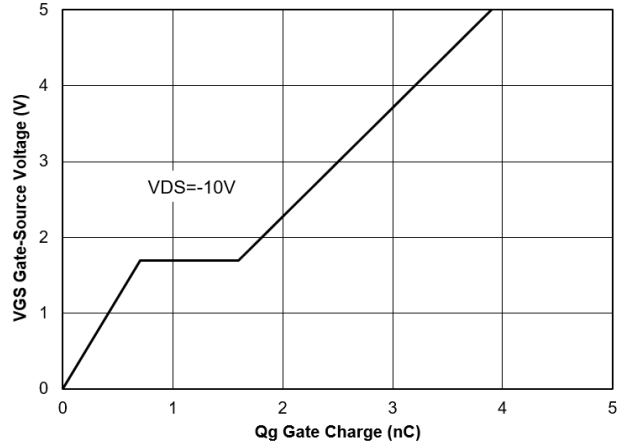


Figure 4. Gate Charge

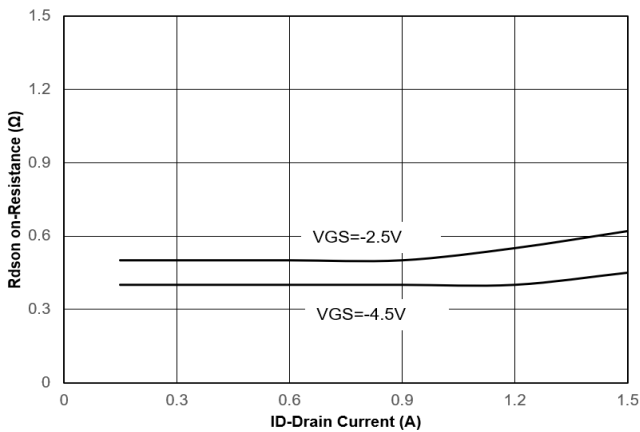


Figure 5. Drain-Source on Resistance

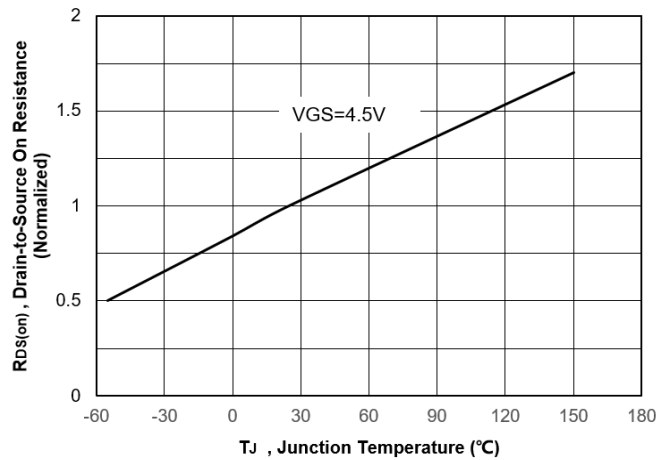


Figure 6. Normalized On-Resistance

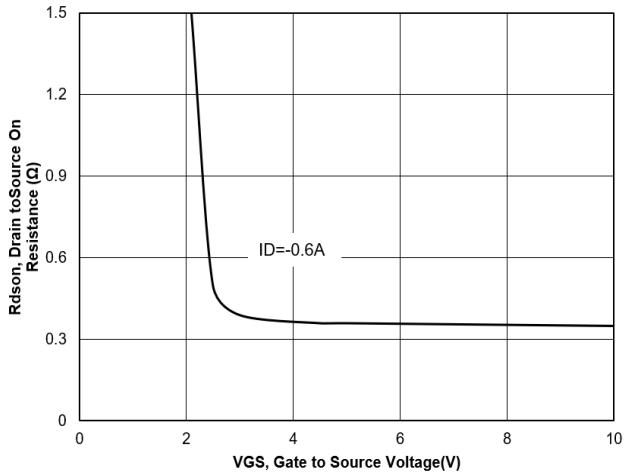


Figure 7. Typical Drain to Source ON Resistance VS Gate Voltage and Drain Current

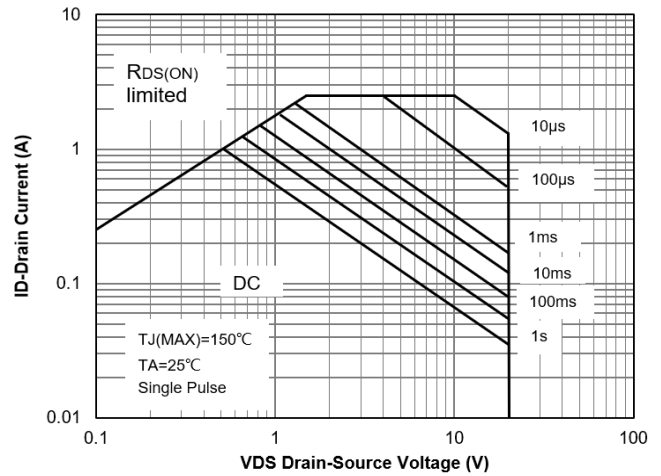


Figure 8. Safe Operation Area

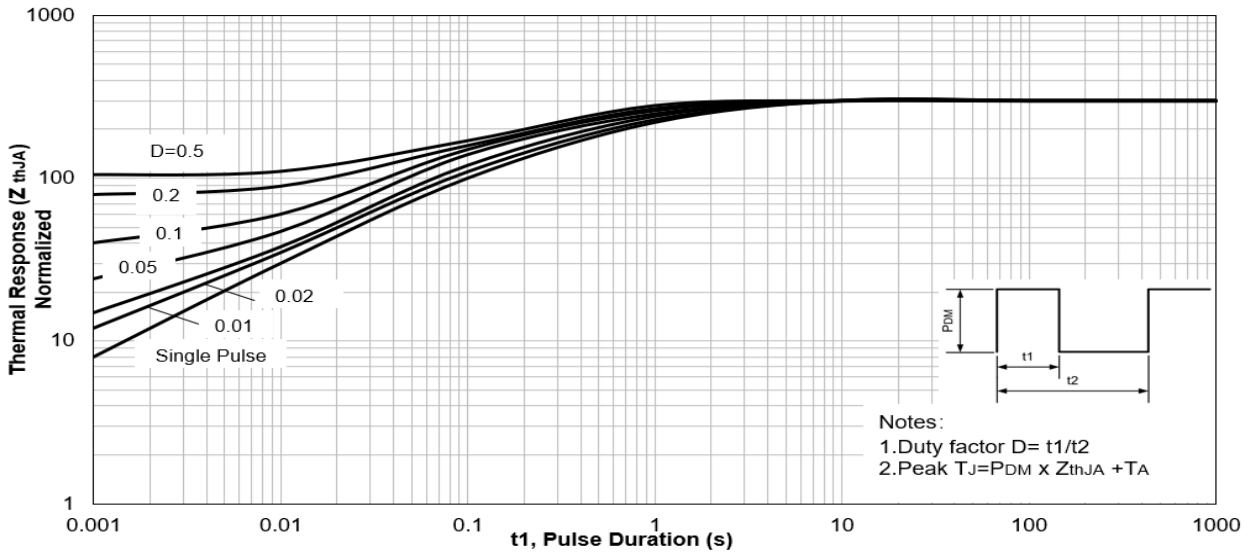


Figure 9. Maximum Effective Transient Thermal Impedance ,Junction-to-Ambient

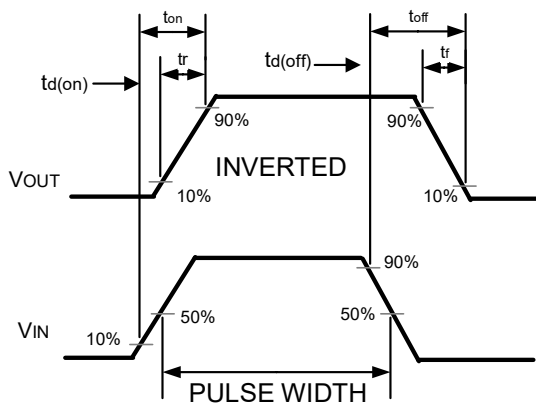
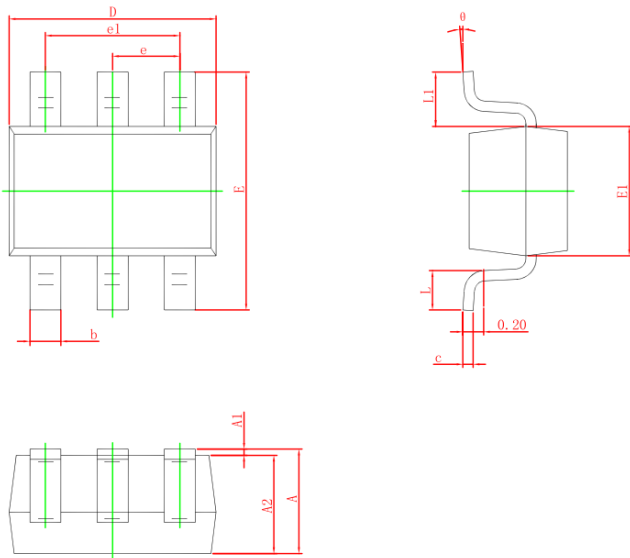


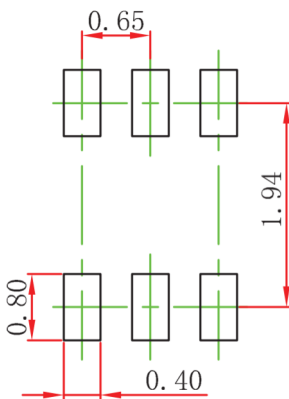
Figure 10. Switching wave

### SOT-363 Package Outline Drawing



Symbol	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.150	0.350	0.006	0.014
c	0.080	0.150	0.003	0.006
D	2.000	2.200	0.079	0.087
E	2.150	2.450	0.085	0.096
E1	1.150	1.350	0.045	0.053
e	0.650 TYP.		0.026 TYP.	
e1	1.200	1.400	0.047	0.055
L	0.260	0.460	0.010	0.018
L1	0.525 REF.		0.021 REF.	
theta	0°	8°	0°	8°

### Suggested Land Pattern



### Contact Information

Applied Power Microelectronics Inc.

Website: <http://www.appliedpowermicro.com>

Email: [sales@appliedpowermicro.com](mailto:sales@appliedpowermicro.com)

Phone: +86 (0519) 8399 3606