

Description

The ASM712 is designed for asymmetrical (12V to -7V) protection in multi-point data transmission application, The ASM712 replace four discrete components by integrating two 12V and two 7V TVS diodes in a single package. The ASM712 complies with the IEC 61000-4-2 (ESD) with ±30kV air and ±30kV contact discharge. It is assembled into a lead-free SOT-23 package. It is designed to protect components which are connected to data and transmission lines from voltage surges.

Features

- 325W peak pulse power (8/20µs)
- Ultra low leakage: nA level
- Operating voltage: 7V or 12V
- Low clamping voltage
- Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test

Air discharge: ±30kV
Contact discharge: ±30kV

- IEC61000-4-5 (Lightning) 13A (8/20µs)

RoHS Compliant

Mechanical Characteristics

Package: SOT-23Lead Finish: Matte Tin

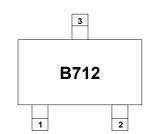
Case Material: "Green" Molding Compound.Terminal Connections: See Diagram Below

Marking Information: See Below

Applications

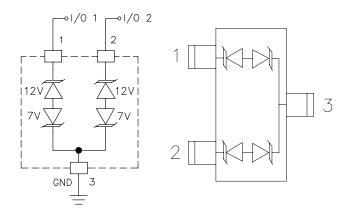
- Wireless System
- Networks
- Portable Instrumentation
- RS485 Ports

Marking Information



B712 = Device Marking Code

Dimensions and Pin Configuration



Circuit Schematic

Pin Schematic

Ordering Information

Part Number	Packaging	Reel Size
ASM712	3000/Tape & Reel	7 inch



Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

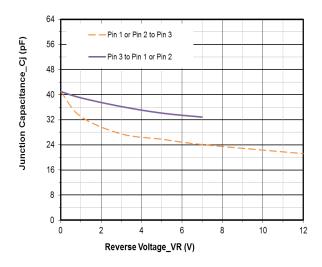
Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20µs)	Ppk	325	W
Peak Pulse Current (8/20µs)	lpp	13	А
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	Vesd	±30 ±30	kV
Operating Temperature Range	TJ	−55 to +125	°C
Storage Temperature Range	Tstg	−55 to +150	°C

Electrical Characteristics (T_A=25°C unless otherwise specified)

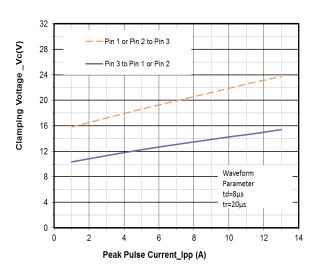
		Pin 1 to 3 and 2 to 3		Pin 3 to 1 and 3 to 2					
Parameter	Symbol	Min	Тур	Max	Min	Тур	Max	Unit	Test Condition
Reverse Working Voltage	VRWM			12			7	V	
Breakdown Voltage	VBR	13.3			7.5			V	IT = 1mA
Reverse Leakage Current	I _R			0.5			0.5	μΑ	VR = VRWM
Clamping Voltage	Vc			20			14	V	IPP = 5A (8 x 20μs pulse)
Clamping Voltage	Vc			25			16	V	IPP = 13A (8 x 20µs pulse)
Junction Capacitance	CJ		40			40		pF	VR = 0V, f = 1MHz
Junction Capacitance	Cl		20			30		pF	VR = VRWM, f = 1MHz



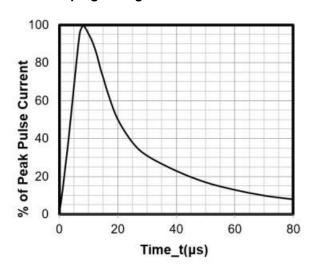
Typical Performance Characteristics (T_A=25°C unless otherwise Specified)



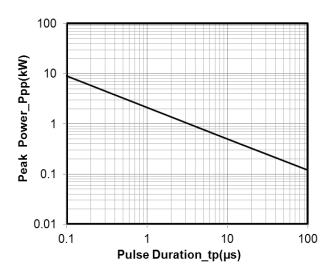
Junction Capacitance vs. Reverse Voltage



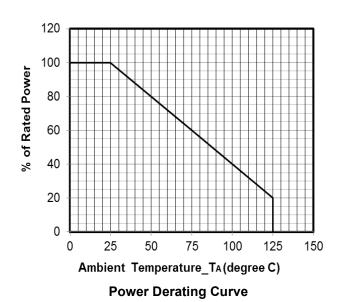
Clamping Voltage vs. Peak Pulse Current

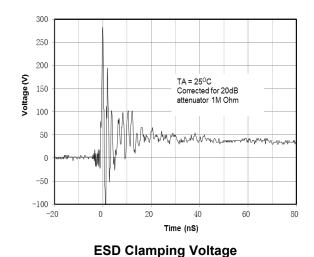


8 X 20µs Pulse Waveform



Peak Pulse Power vs. Pulse Time



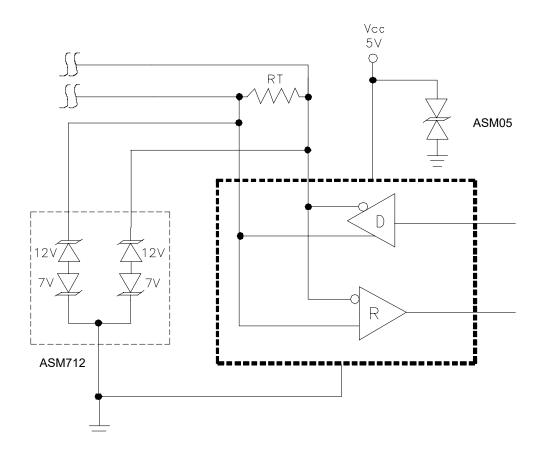


8 kV Contact per IEC61000-4-2



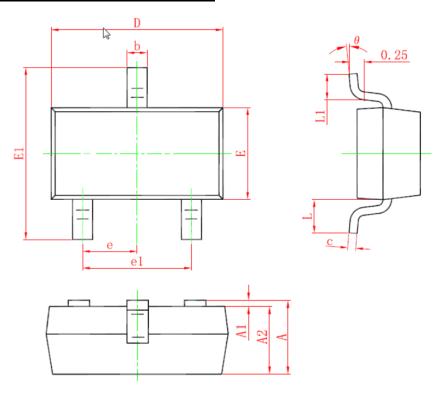
ASM712 on RS-485 Data Lines Application

EIA RS-485 specifies a ±7V ground difference between devices on the bus. This permits the bus voltage to range from +12V (5V + 7V) to -7V (0 - 7V). The ASM712 is designed to protect two RS-485 data lines in extended common mode applications. The ASM712 may be used to protect devices from transient voltages resulting from ESD, EFT, and light ning. The device is designed with asymmetrical operating voltages for optimum protection. The TVS diodes at pins 1 and 2 have a working voltage of 12volts. These pins are connected to the differential data line pairs. The TVS diodes at pin 3 have a working voltage of 7volts. Pin 3 is connected to ground. The internal TVS diodes of the ASM712 will protect the transceiver input from positive transient voltage spikes greater than 12V and negative spikes greater than 7V.





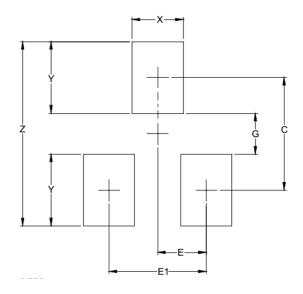
SOT-23 Package Outline Drawing



	DIMENSIONS					
->	N	<i>MILLIMETER</i>	S	INCHES		
SYM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.90		1.15	0.035		0.045
A1	0.00		0.10	0.000		0.004
A2	0.90		1.05	0.035		0.041
b	0.30		0.50	0.012		0.020
С	0.08		0.15	0.003		0.006
D	2.80		3.00	0.110		0.118
E	1.20		1.40	0.047		0.055
E1	2.25		2.55	0.089		0.100
е		0.95TYP		0.037TYP		
e1	1.80		2.00	0.071		0.079
L	0.55REF			0.022REF		
L1	0.30		0.50	0.012		0.020
Θ	0°		8°	0°		8°



Suggested Land Pattern



SYM	DIMENSIONS				
	INCHES	MILLIMETERS			
С	0.087	(2.20)			
E	0.037	0.95			
E1	0.075	1.90			
G	0.031	0.80			
Х	0.039	1.00			
Υ	0.055	1.40			
Z	0.141	3.60			

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