

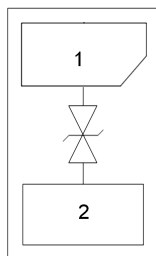
Description

The AR3621P0 is a bi-directional TVS diode, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive high-speed data lines. The AR3621P0 has an ultra-low capacitance with a typical value at 0.3pF, and complies with the IEC 61000-4-2 (ESD) with $\pm 15\text{kV}$ air and $\pm 8\text{kV}$ contact discharge. It is assembled into an ultra-small 0.6x0.3x0.3mm lead-free DFN package. The small size, ultra-low capacitance and high ESD surge protection make AR3621P0 an ideal choice to protect cell phone, digital visual interfaces, HDMI, DVI, USB2.0, USB3.0, and other high speed ports.

Features

- Ultra small package: 0.6x0.3x0.3mm
- Ultra low capacitance: 0.3pF typical
- Ultra low leakage: nA level
- Operating voltage: 36V
- Low clamping voltage
- 2-Pin leadless package
- Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 - Air discharge: $\pm 15\text{kV}$
 - Contact discharge: $\pm 8\text{kV}$
- RoHS Compliant

Dimensions and Pin Configuration



Circuit and Pin Schematic

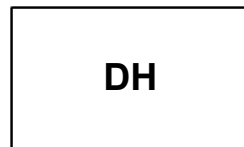
Mechanical Characteristics

- Package: DFN0603-2
- Case Material: “Green” Molding Compound.
- Terminal Connections: See Diagram Below
- Marking Information: See Below

Applications

- Cellular Handsets and Accessories
- Display Ports
- MDDI Ports
- USB Ports
- Digital Visual Interface (DVI)
- PCI Express and Serial SATA Ports

Marking Information



DH = Device Marking Code

Ordering Information

Part Number	Packaging	Reel Size
AR3621P0	10000/Tape & Reel	7 inch

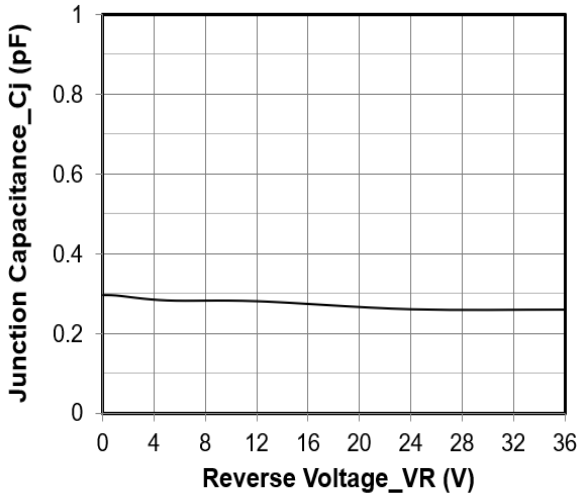
Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20 μs)	Ppk	50	W
ESD per IEC 61000-4-2 (Air)	VESD	± 15	kV
ESD per IEC 61000-4-2 (Contact)		± 8	
Operating Temperature Range	TJ	-55 to +125	$^{\circ}\text{C}$
Storage Temperature Range	Tstg	-55 to +150	$^{\circ}\text{C}$

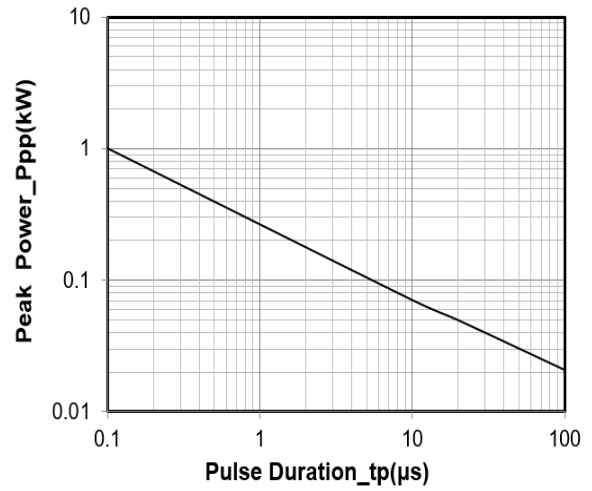
Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Working Voltage	VRWM			36	V	
Breakdown Voltage	VBR	37			V	$I_T = 1\text{mA}$
Reverse Leakage Current	I_R			0.5	μA	VRWM = 36V
TLP Clamping Voltage	Vc		45		V	$I_{TLP} = 4\text{A}$
TLP Clamping Voltage	Vc		72		V	$I_{TLP} = 16\text{A}$
Junction Capacitance	CJ		0.3		pF	VR = 0V, f = 1MHz

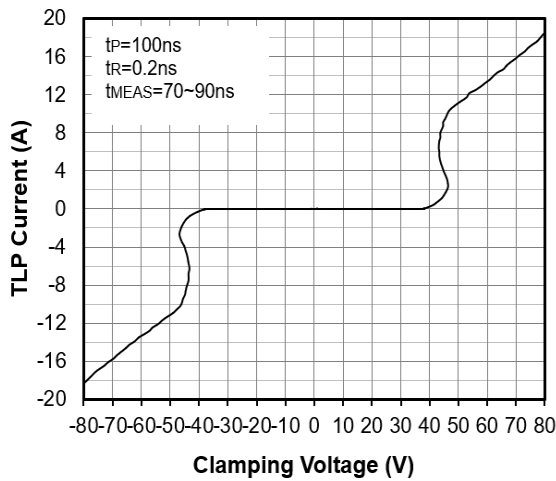
Typical Performance Characteristics (TA=25°C unless otherwise Specified)



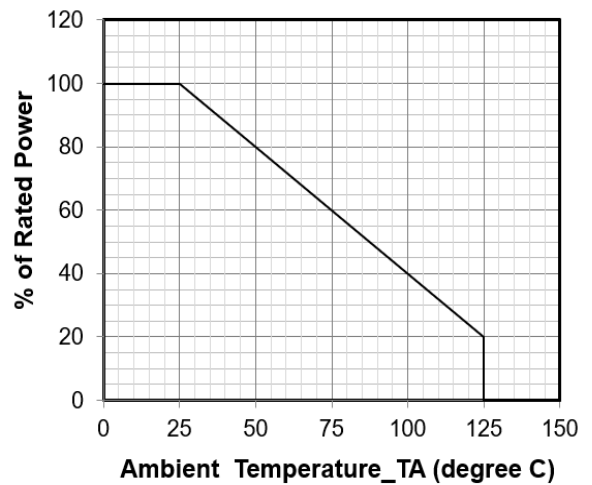
Junction Capacitance vs. Reverse Voltage



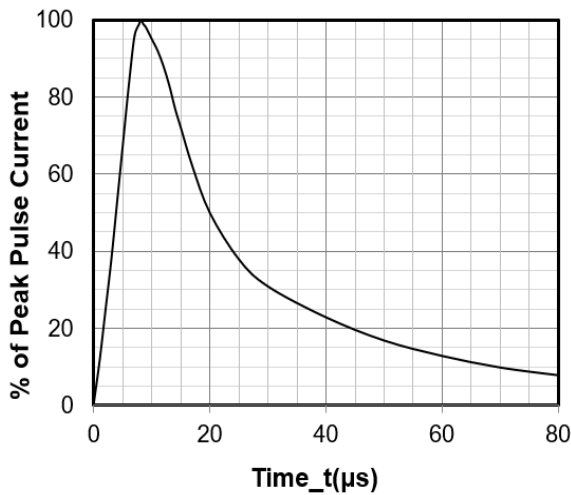
Peak Pulse Power vs. Pulse Time



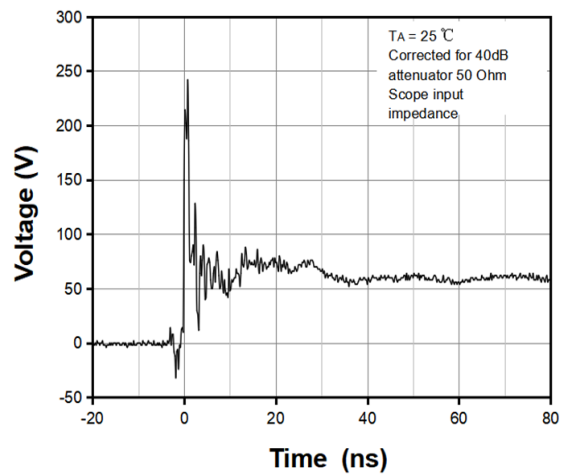
TLP Measurement



Power Derating Curve



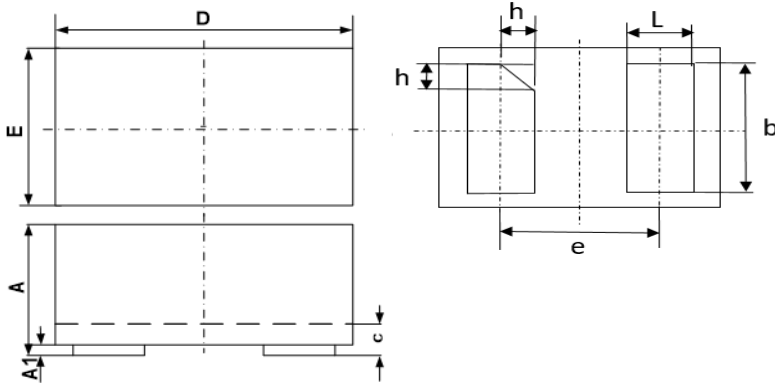
8 X 20μs Pulse Waveform



ESD Clamping Voltage

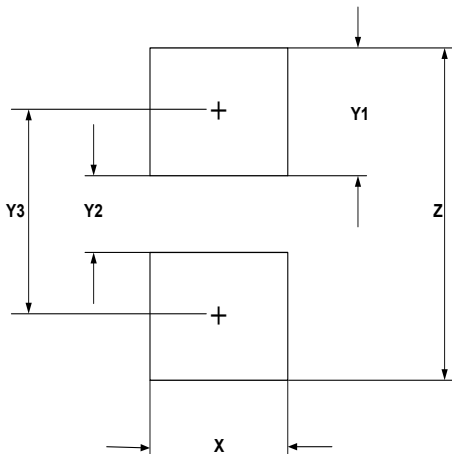
8 kV Contact per IEC61000-4-2

DFN0603-2 Package Outline Drawing



SYM	DIMENSIONS		
	MILLIMETERS		
	MIN	NOM	MAX
A	0.280	--	0.320
A1	0.000	0.020	0.050
b	0.200	0.245	0.300
c	0.050	0.100	0.150
D	0.550	0.600	0.650
e	0.380 BSC		
E	0.250	0.300	0.350
L	0.130	0.190	0.240
h	0.070 BSC		

Suggested Land Pattern



SYM	DIMENSIONS	
	MILLIMETERS	INCHES
X	0.30	0.012
Y1	0.25	0.010
Y2	0.15	0.006
Y3	0.40	0.016
Z	0.65	0.026

Contact Information

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