

Description

The AR3321P0S is a bi-directional TVS diode, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive high-speed data lines. The AR3321P0S has a low capacitance with a typical value at 0.6pF, and complies with the IEC 61000-4-2 (ESD) with $\pm 30\text{kV}$ air and $\pm 30\text{kV}$ contact discharge. It is assembled into a DFN0603-2 leadfree package. The small size, low capacitance and high ESD surge protection make AR3321P0S an ideal choice to protect cell phone, digital video interfaces and other high speed ports.

Features

- low capacitance: 0.6pF typical
- low leakage: nA level
- Operating voltage: 3.3V
- Low clamping voltage
- Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 - Air discharge: $\pm 30\text{kV}$
 - Contact discharge: $\pm 30\text{kV}$
 - IEC61000-4-5 (Lightning) 10A (8/20 μs)
- RoHS Compliant

Mechanical Characteristics

- Package: DFN0603-2
- Case Material: “Green” Molding Compound.
- Terminal Connections: See Diagram Below
- Marking Information: See Below

Applications

- Cellular Handsets and Accessories
- Display Ports
- MDDI Ports
- USB Ports
- Digital Visual Interface (DVI)
- PCI Express and Serial SATA Ports



Caution:

This Device is designed for signal line protection only.

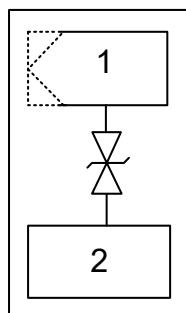
Not intended to be used under bias, not for application with a power line.

Marking Information



3Z = Device Marking Code

Equivalent Circuit and Pin Configuration



Circuit and Pin Schematic

Ordering Information

Part Number	Packaging	Reel Size
AR3321P0S	10000/Tape & Reel	7 inch

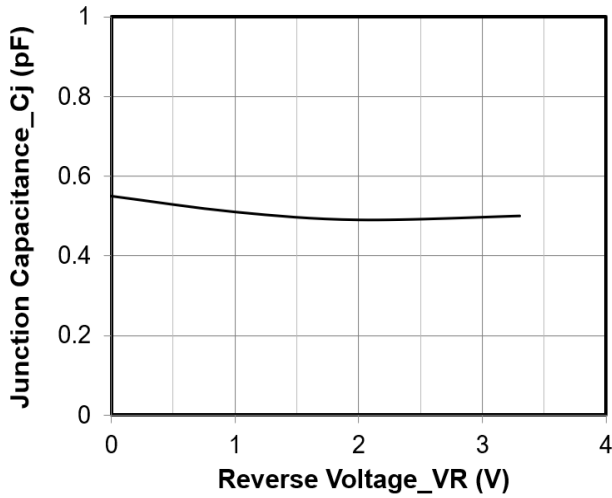
Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20 μs)	Ppk	50	W
Peak Pulse Current (8/20 μs)	I _{PP}	10	A
ESD per IEC 61000-4-2 (Air)	V _{ESD}	± 30	kV
ESD per IEC 61000-4-2 (Contact)		± 30	
Operating Temperature Range	T _J	-55 to +125	$^\circ\text{C}$
Storage Temperature Range	T _{stg}	-55 to +150	$^\circ\text{C}$

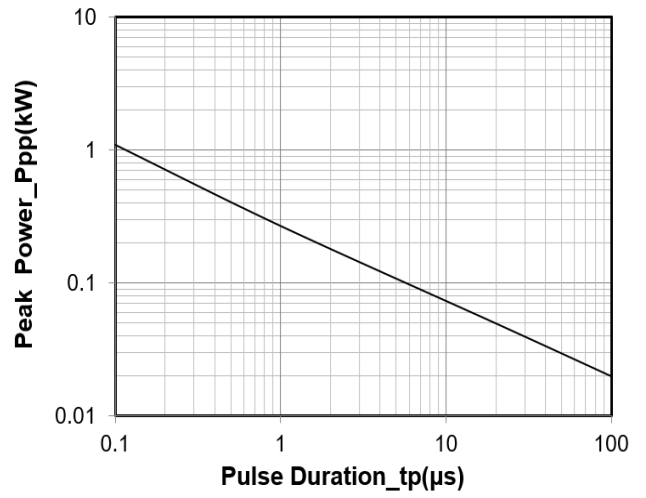
Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Working Voltage	V _{RWM}			3.3	V	
Punch-Through Voltage	V _{PT}	3.5			V	I _T = 2 μA
Snap-Back Voltage	V _{SB}	0.8			V	I _T = 50mA
Reverse Leakage Current	I _R			0.2	μA	V _{RWM} = 3.3V
Clamping Voltage	V _C		1.5		V	I _{PP} = 1A (8 x 20 μs pulse)
Clamping Voltage	V _C		4		V	I _{PP} = 10A (8 x 20 μs pulse)
Junction Capacitance	C _J		0.6		pF	V _R = 0V, f = 1MHz

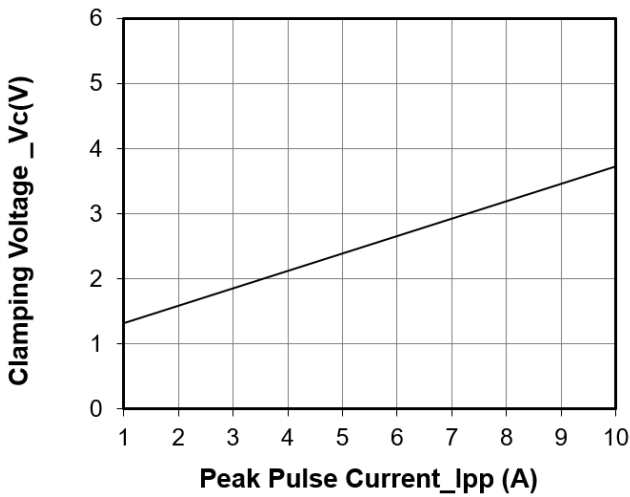
Typical Performance Characteristics (TA=25°C unless otherwise Specified)



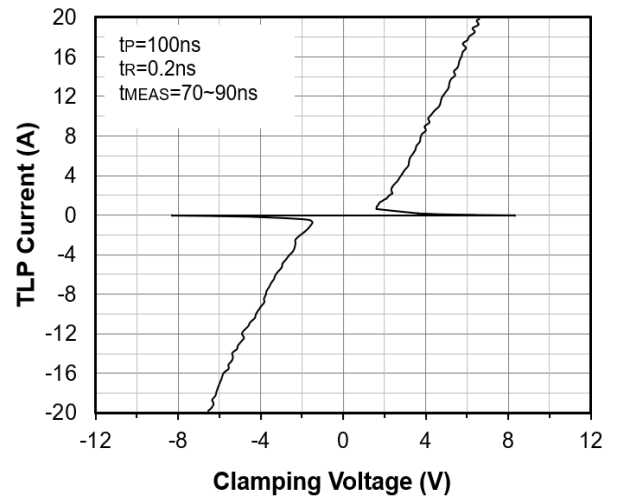
Junction Capacitance vs. Reverse Voltage



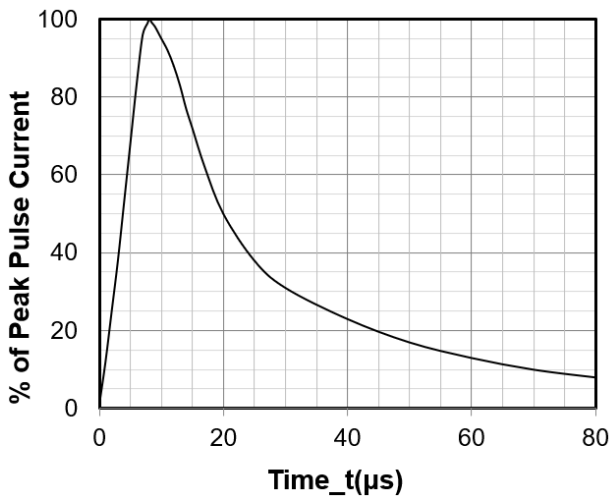
Peak Pulse Power vs. Pulse Time



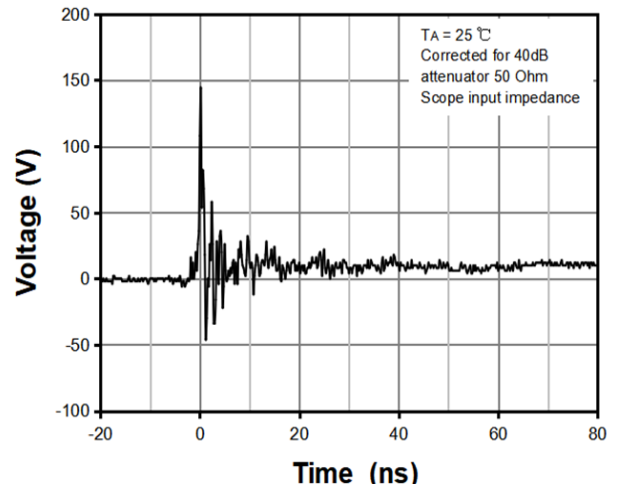
Clamping Voltage vs. Peak Pulse Current (tp = 8/20μs)



TLP Measurement

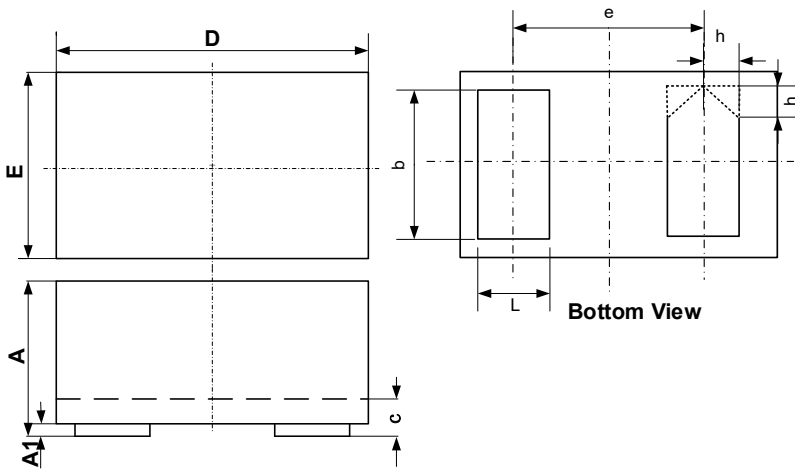


8 X 20μs Pulse Waveform



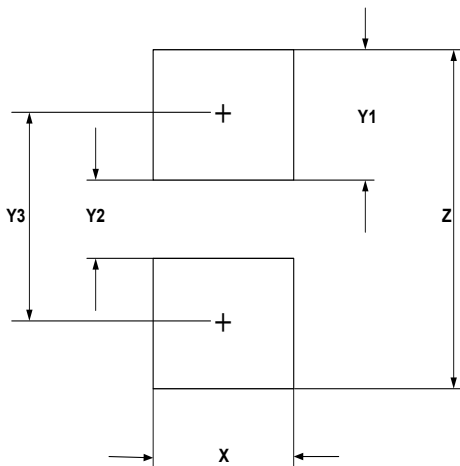
ESD Clamping Voltage
8 kV Contact per IEC61000-4-2

DFN0603-2 Package Outline Drawing



SYM	DIMENSIONS		
	MILLIMETERS		
	MIN	NOM	MAX
A	0.230	-	0.350
A1	0.000	0.020	0.050
b	0.200	0.245	0.300
c	0.050	0.150	0.180
D	0.550	0.600	0.650
e	0.355 BSC		
E	0.250	0.300	0.350
L	0.130	0.190	0.240
h	0.079 BSC		

Suggested Land Pattern



SYM	DIMENSIONS	
	MILLIMETERS	INCHES
X	0.30	0.012
Y1	0.25	0.010
Y2	0.15	0.006
Y3	0.40	0.016
Z	0.65	0.026

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