

Description

The AR3321P0SC is a bi-directional TVS diode, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive high-speed data lines. The AR3321P0SC has a low capacitance with a typical value at 0.3pF, and complies with the IEC 61000-4-2 (ESD) with ±15kV air and ±15kV contact discharge. The small size, low capacitance and high ESD surge protection make AR3321P0SC an ideal choice to protect cell phone, digital visual interfaces, HDMI, DVI, USB2.0, USB3.0, and other high speed ports.

Features

low capacitance: 0.3pF typical

Ultra low leakage: nA level

Operating voltage: 3.3V

Low clamping voltage

2-pin leadless package

Complies with following standards:

IEC 61000-4-2 (ESD) immunity test

Air discharge: ±15kV
Contact discharge: ±15kV

- IEC61000-4-5 (Lightning) 5A (8/20µs)

RoHS Compliant

Mechanical Characteristics

• Package: DFN0603-2

• Case Material: "Green" Molding Compound.

Terminal Connections: See Diagram Below

• Marking Information: See Below

Applications

- Cellular Handsets and Accessories
- Display Ports
- MDDI / MHL
- USB 2.0 / USB 3.0
- Digital Visual Interface (DVI)
- PCI Express and Serial SATA Ports

Caution:



This Device is designed for signal line protection only.

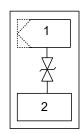
Not intended to be used under bias, not for application with a power line.

Marking Information

1B

1B = Device Marking Code

Equivalent Circuit and Pin Configuration



Ordering Information

Part Number	Packaging	Reel Size
AR3321P0SC	10000/Tape & Reel	7 inch

Circuit and Pin Schematic



Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20µs)	Ppk	35	W
Peak Pulse Current (8/20µs)	IPP	5	А
ESD per IEC 61000-4-2 (Air)	VESD	±15	kV
ESD per IEC 61000-4-2 (Contact)	VESD	±15	K V
Operating Temperature Range	TJ	−55 to +125	°C
Storage Temperature Range	Tstg	−55 to +150	°C

Electrical Characteristics (T_A=25°C unless otherwise specified)

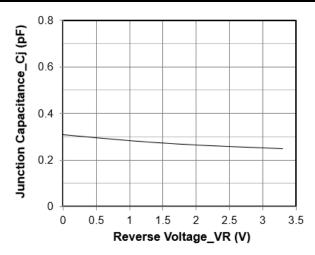
Parameter	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Working Voltage	VRWM			3.3	V	
Breakdown Voltage	VBR	3.5			V	IT = 1mA
Reverse Leakage Current	lR			0.2	μA	VRWM = 3.3V
Clamping Voltage	Vc			7	V	IPP = 5A (8 x 20µs pulse)
ESD Clamping Voltage ⁽¹⁾	Vc		5.1		V	IPP = 4A, tp = 0.2/100ns (TLP)
	VC		9.1		V	IPP = 16A, tp = 0.2/100ns (TLP)
Dynamic Resistance ⁽²⁾	R _{DYN}		0.36		Ohm	tp = 0.2/100ns (TLP)
Junction Capacitance	CJ		0.3		pF	VR = 0V, f = 1MHz

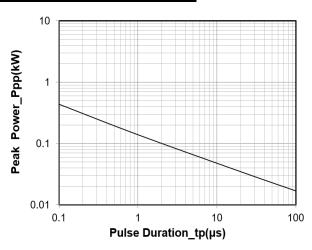
⁽¹⁾ Transmission Line Pulse Test (TLP) Settings: tp = 100ns, tr = 0.2ns.

⁽²⁾ Dynamic resistance calculated from ITLP = 4A to ITLP = 16A.

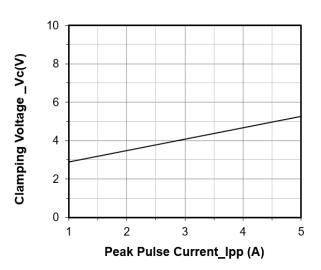


Typical Performance Characteristics (TA=25°C unless otherwise Specified)

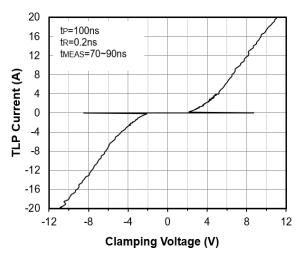




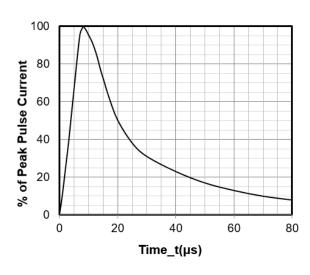
Junction Capacitance vs. Reverse Voltage



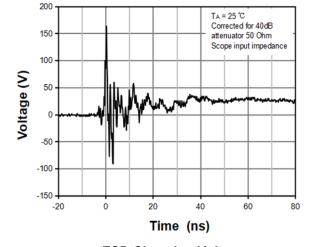
Peak Pulse Power vs. Pulse Time



Clamping Voltage vs. Peak Pulse Current (tp = 8/20µs)



TLP Measurement

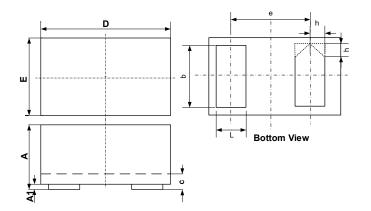


8 X 20µs Pulse Waveform

ESD Clamping Voltage 8 kV Contact per IEC61000-4-2

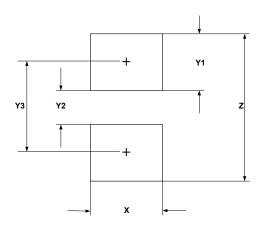


DFN0603-2 Package Outline Drawing



	DIMENSIONS		
	MILLIMETERS		
SYM	MIN	NOM	MAX
Α	0.230		0.340
A1	0.000	-	0.050
b	0.200	-	0.300
С	0.050	-	0.180
D	0.550	-	0.650
е	0.360 BSC		
Е	0.250	-	0.350
L	0.130	-	0.240
h	0.079 BSC		

Suggested Land Pattern



SYM	DIMENSIONS			
	MILLIMETERS	INCHES		
Х	0.30	0.012		
Y1	0.25	0.010		
Y2	0.15	0.006		
Y3	0.40	0.016		
Z	0.65	0.026		

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