

Description

The AR3306PA is an ultra low capacitance TVS array, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive high-speed data lines. The AR3306PA has an ultra-low capacitance with a typical value at 0.3pF, and complies with the IEC 61000-4-2 (ESD) standard with ±25kV air and ±20kV contact discharge. The flow through style package allows for easy PCB layout and matched trace lengths necessary to maintain consistent impedance between high speed differential lines. The small size, ultra-low capacitance and high ESD surge protection make AR3306PA an ideal choice to protect HDMI 1.4, USB 3.0 and other high speed ports.

Features

- Ultra low capacitance: 0.3pF typical (I/O to I/O)
- Ultra low leakage: nA level
- Low operating voltage: 3.3V
- Low clamping voltage
- Protects one power line and six data lines
- Leadless flow-through package
- Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 Air discharge: ±25kV
 Contact discharge: ±20kV
 - IEC61000-4-5 (Lightning) 5A (8/20μs)
- RoHS Compliant

Dimensions and Pin Configuration



(BOTTOM VIEW)



Circuit Diagram

Pin Schematic

Mechanical Characteristics

- Package: DFN4120-10 (4.1×2.0×0.55mm)
- Case Material: "Green" Molding Compound.
- Terminal Connections: See Diagram Below
- Marking Information: See Below

Applications

- USB 3.0
- HDMI 1.4
- High-Speed Data Lines

Marking Information



3306 = Device Marking Code Dot denotes pin1

Ordering Information

Part Number	Packaging	Reel Size
AR3306PA	3000/Tape & Reel	7 inch



Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20µs)	Ppk	80	W
Peak Pulse Current (8/20µs)	IPP	5	А
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	Vesd	±25 ±20	kV
Operating Temperature Range	TJ	−55 to +125	°C
Storage Temperature Range	Tstg	−55 to +150	°C

Electrical Characteristics (T_A=25°C unless otherwise specified)

Parameter	Symbol	Min	Тур	Мах	Unit	Test Condition
Reverse Working Voltage	VRWM			3.3	V	Any I/O pin to ground
Breakdown Voltage	Vbr	5			V	IT = 1mA, any I/O pin to ground
Reverse Leakage Current	I _R			0.5	μA	VRWM = 3.3V, any I/O pin to ground
Clamping Voltage	Vc			9	V	IPP = 1A (8 x 20µs pulse), any I/O pin to ground
Clamping Voltage	Vc			16	V	IPP = 5A (8 x 20µs pulse), any I/O pin to ground
Junction Capacitance	CJ		0.30	0.40	pF	Vcc = 5V, VIN = 2.5V, f = 1MHz, any I/O pin to I/O



AR3306PA



Typical Performance Characteristics (T_A=25°C unless otherwise Specified)

Junction Capacitance vs. Reverse Voltage



Clamping Voltage vs. Peak Pulse Current



8 X 20µs Pulse Waveform



Peak Pulse Power vs. Pulse Time









Typical Application

The AR3306PA is designed for easy PCB layout by allowing the traces to run straight through the device. The protected data lines are normally connected at pins 1, 4, 5, 6, 7 &10, pin 9 is connected to ground. The connection to ground should be made directly to a ground plane. The path length should also be kept as short as possible to minimize parasitic inductance. Pin 2 can be connected to Vcc biased or left not connected depending upon the application.



AR3306PA on USB3.0 Application





DFN4120-10 Package Outline Drawing





	DIMENSIONS					
0)/14	MILLIMETERS			INCHES		
5YM	MIN	NOM	MAX	MIN	NOM	MAX
А	0.50	0.55	0.60	0.020	0.022	0.024
A1	0.00		0.05	0.000		0.002
A3	0.15 REF			0.006 REF		
D	4.05	4.10	4.15	0.162	0.164	0.166
Е	1.95	2.00	2.05	0.075	0.080	0.082
D2	1.25	1.40	1.50	0.050	0.056	0.060
E2	0.65	0.80	0.90	0.026	0.032	0.036
b	0.15	0.20	0.25	0.006	0.008	0.010
L	0.20	0.30	0.40	0.008	0.012	0.016
e1	0.40 BSC			0.016 BSC		
е	0.80 BSC			(0.032 BS	0

BOTTOM VIEW

Suggested Land Pattern



Contact Information

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evm	DIMENSIONS					
5111	MILLIMETERS	INCHES				
Α	0.800	0.032				
В	0.400	0.016				
С	0.600	0.024				
D	0.200	0.008				
E	0.800	0.032				
F	1.400	0.056				
Н	2.000	0.080				
I	0.300	0.012				
е	0.200	0.008				

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