

Description

The AR0508MP is a low capacitance TVS array, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive high-speed data lines. The AR0508MP has a low capacitance with a typical value at 0.6 pF, and complies with the IEC 61000-4-2 (ESD) with ±25kV air and ±20kV contact discharge. It is assembled into a 10-pin lead-free MSOP package. The flow through style package allows for easy PCB layout and matched trace lengths necessary to maintain consistent impedance between high speed differential lines. The small size, low capacitance and high ESD surge protection make AR0508MP an ideal choice to protect high speed ports.

Features

- Very low capacitance: 0.6pF typical
- Low operating voltage: 5V
- Low clamping voltage
- Protects one power line and six data lines
- Flow-through package
- Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 Air discharge: ±25kV
 Contact discharge: ±20kV

Dimensions and Pin Configuration

- IEC61000-4-5 (Lightning) 5A (8/20µs)
- RoHS Compliant

Mechanical Characteristics

- Package: MSOP-10
- Lead Finish: Matte Tin
- Case Material: "Green" Molding Compound.
- Terminal Connections: See Diagram Below
- Marking Information: See Below

Applications

- DVI Ports
- HDMI Ports
- USB 2.0
- High-Speed Data Lines

Marking Information



Ordering Information

	10
	- 19
D	18
	- Ie



Circuit Diagram

Pin Schematic

Part Number	Packaging	Reel Size
AR0508MP	3000/Tape & Reel	13 inch



Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20µs)	Ppk	80	W
Peak Pulse Current (8/20µs)	IPP	5	А
ESD per IEC 61000-4-2 (Air)	Vesd	±25	kV
ESD per IEC 61000-4-2 (Contact)		±20	
Operating Temperature Range	TJ	−55 to +125	°C
Storage Temperature Range	Tstg	−55 to +150	°C

Electrical Characteristics (T_A=25°C unless otherwise specified)

Parameter	Symbol	Min	Тур	Мах	Unit	Test Condition
Reverse Working Voltage	VRWM			5	V	Pin 3 to 8
Breakdown Voltage	Vbr	6			V	IT = 1mA, Pin 3 to 8
Reverse Leakage Current	I _R			0.2	μA	VRWM = 5V, Pin 3 to 8
Clamping Voltage	Vc			10	V	IPP = 1A (8 x 20µs pulse), any I/O pin to ground
Clamping Voltage	Vc			16	V	IPP = 5A (8 x 20µs pulse), any I/O pin to ground
Junction Capacitance	CJ		0.3	0.5	pF	VR = 0V, f = 1MHz, between I/O pins
Junction Capacitance	CJ		0.6		pF	VR = 0V, f = 1MHz, any I/O pin to ground







Typical Performance Characteristics (T_A=25°C unless otherwise Specified)

Junction Capacitance vs. Reverse Voltage



Clamping Voltage vs. Peak Pulse Current



8 X 20µs Pulse Waveform



Peak Pulse Power vs. Pulse Time









MSOP-10 Package Outline Drawing





DIMENSIONS MILLIMETERS INCHES DIM MIN NOM MAX MIN NØMX A1 A2 . 007 b D 114 E1 . 114 118 2.90 3.00 3.10 90 E E 50 е L L1 Ν θ1 0.40 0.60 0.80 004 aaa 003 bbb 0.08

NOTES:

1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).

2. DATUMS -A- AND -B- TO BE DETERMINED AT DATUM PLANE

 DIMENSIONS "E1" AND "D" DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.

4. REFERENCE JEDEC STD MO-187, VARIATION BA.

Suggested Land Pattern



-H-

DIM INCHES MILLIMETERS C (. 161) (4. 10) G . 098 2. 50 P . 020 0. 50 X . 011 0. 30 Y . 063 1. 60	DIMENSIONS				
C (. 161) (4.10) G .098 2.50 P .020 0.50 X .011 0.30 Y .063 1.60	DIM	INCHES	MILLIMETERS		
G .098 2.50 P .020 0.50 X .011 0.30 Y .063 1.60	С	(. 161)	(4.10)		
P .020 0.50 X .011 0.30 Y .063 1.60	G	. 098	2.50		
X .011 0.30 Y 063 1.60	Р	. 020	0.50		
Y 063 1.60	Х	.011	0.30		
1 .000 1.00	Y	.063	1.60		
Z . 224 5.70	Ζ	. 224	5.70		

NOTES:

1. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY. CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.

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