

### **Description**

The AR0504S2A is an ultra low capacitance TVS array, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive high-speed data lines. The AR0504S2A complies with the IEC 61000-4-2 (ESD) with ±30kV air and ±25kV contact discharge. It is assembled into a 6-Pin lead-free SOT23-6 package. The low capacitance array make it ideal for four high speed data and transmission line. This device is optimized for ESD protection of portable electronics.

### **Features**

- Ultra low leakage: nA level
- Operating voltage: 5V
- Low clamping voltage
- Up to four data lines and one power line protects
- Complies with following standards:
  - IEC 61000-4-2 (ESD) immunity test
     Air discharge: ±30kV
     Contact discharge: ±25kV
  - IEC61000-4-5 (Lightning) 5A (8/20μs)
- RoHS Compliant

#### **Mechanical Characteristics**

Package: SOT23-6Lead Finish: Matte Tin

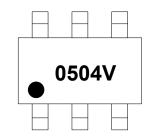
Case Material: "Green" Molding Compound.Terminal Connections: See Diagram Below

Marking Information: See Below

## **Applications**

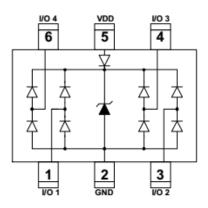
- USB 2.0 Power and Data lines protection
- Digital Visual Interface (DVI)
- Monitors and Flat Panel Displays
- Video Graphic Cards
- Notebook and PC Computers

## **Marking Information**



0504V= Device Marking Code Dot denotes Pin1

# **Equivalent Circuit and Pin Configuration**



Circuit and Pin Schematic

### **Ordering Information**

Part Number	Packaging	Reel Size
AR0504S2A	3000/Tape & Reel	7 inch



# Absolute Maximum Ratings (T<sub>A</sub>=25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20µs)	Ppk	60	W
Peak Pulse Current (8/20µs)	IPP	5	А
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	VESD	±30 ±25	kV
Operating Temperature Range	TJ	−55 to +125	°C
Storage Temperature Range	Tstg	-55 to +150	°C

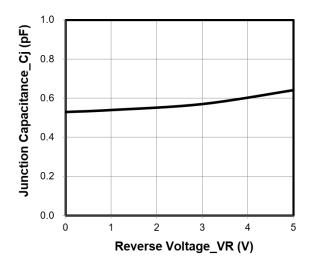
# Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise specified)

Parameter	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Working Voltage	VRWM			5	V	Any I/O pin to ground
Breakdown Voltage	VBR	6			V	IT = 1mA, any I/O pin to ground
Reverse Leakage Current	I <sub>R</sub>			0.5	μΑ	VRWM = 5V, any I/O pin to ground
Clamping Voltage	Vc			9.5	V	IPP = 1A (8 x 20µs pulse), any I/O pin to ground
Clamping Voltage	Vc			12	V	IPP = 5A (8 x 20μs pulse), any I/O pin to ground
Junction Capacitance	Cl		0.3	0.4	pF	VR = 0V, f = 1MHz, between I/O pins
Junction Capacitance	Cl		0.5	0.8	pF	VR = 0V, f = 1MHz, any I/O pin to ground

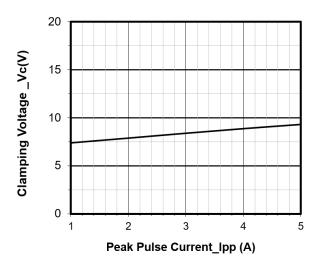
Note 1: I/O pins are Pin 1, 3, 4 and 6



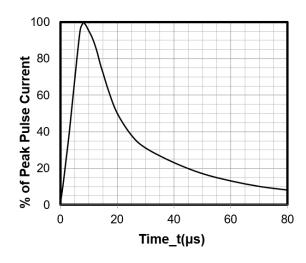
# Typical Performance Characteristics (T<sub>A</sub>=25°C unless otherwise Specified)



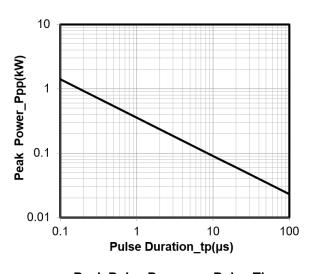
Junction Capacitance vs. Reverse Voltage



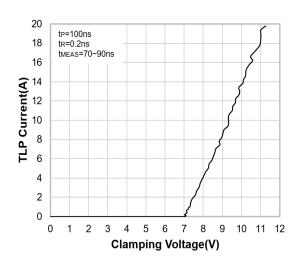
Clamping Voltage vs. Peak Pulse Current



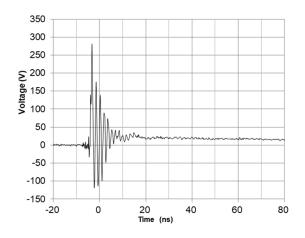
8 X 20µs Pulse Waveform



Peak Pulse Power vs. Pulse Time



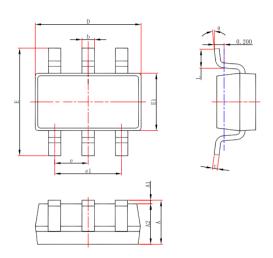
**TLP Measurement** 



ESD Clamping Voltage 8 kV Contact per IEC61000-4-2

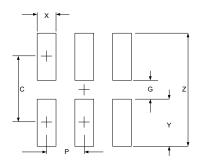


## **SOT23-6 Package Outline Drawing**



C	Dimensions I	n Millimeters	Dimensions	s In Inches
Symbol	Min.	Max.	Min.	Max.
Α	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
С	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
Е	2.650	2.950	0.104	0.116
е	0.950	(BSC)	0.037	(BSC)
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

### **Suggested Land Pattern**



OVM	DIMENSIONS			
SYM	MILLIMETERS	INCHES		
С	2.50	0.098		
G	1.40	0.055		
Р	0.95	0.037		
Х	0.60	0.024		
Υ	1.10	0.043		
Z	3.60	0.141		

# **Contact Information**

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