

Description

The AR0502PN is an Uni-directional TVS diode, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive high-speed data lines. The AR0502PN has an ultra-low capacitance with a typical value at 0.2pF, and complies with the IEC 61000-4-2 (ESD) with ±25kV air and ±20kV contact discharge. It is assembled into an ultra-small 1.2x1.0x0.5mm lead-free DFN package. The small size, ultra-low capacitance and high ESD surge protection make AR0502PN an ideal choice to protect cell phone, digital visual interfaces and other high speed ports.

Features

- Ultra low leakage: nA level
- Low operating voltage: 5V
- Low clamping voltage
- 6-pin leadless package
- Up to 2-line protects
- Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 Air discharge: ±25kV
 Contact discharge: ±20kV
 - IEC61000-4-5 (Lightning) 3A (8/20µs)
- RoHS Compliant

Mechanical Characteristics

- Package: DFN1210-6
- Case Material: "Green" Molding Compound.
- Terminal Connections: See Diagram Below
- Marking Information: See Below

Applications

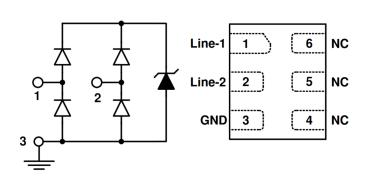
- Cellular Handsets and Accessories
- USB Ports
- Digital Visual Interface
- MMC/SD Ports

Marking Information



52N = Device Marking Code Dot denotes Pin1

Dimensions and Pin Configuration



Ordering Information

Part Number	Packaging	Reel Size
AR0502PN	3000/Tape & Reel	7 inch



Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

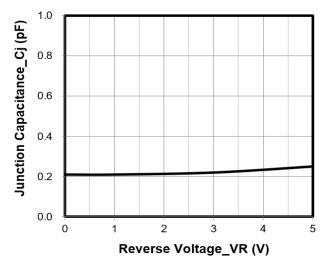
Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20µs)	Ppk	50	W
Peak Pulse Current (8/20µs)	I PP	3	Α
ESD per IEC 61000-4-2 (Air)	VESD	±25	kV
ESD per IEC 61000-4-2 (Contact)		±20	
Operating Temperature Range	TJ	-55 to +125	°C
Storage Temperature Range	Tstg	−55 to +150	°C

Electrical Characteristics (T_A=25°C unless otherwise specified)

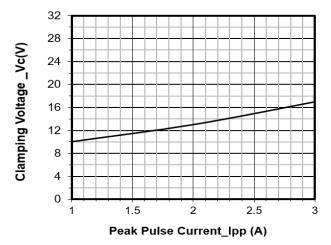
Parameter	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Working Voltage	VRWM			5	V	
Breakdown Voltage	VBR	6			V	IT = 1mA
Reverse Leakage Current	I _R			0.2	μA	VRWM = 5V
Clamping Voltage	Vc			10	V	IPP = 1A (8 x 20μs pulse), any I/O pin to ground
Clamping Voltage	Vc			17	V	IPP = 3A (8 x 20μs pulse), any I/O pin to ground
Junction Capacitance	CJ		0.2		pF	VR = 0V, f = 1MHz, any I/O pin to ground



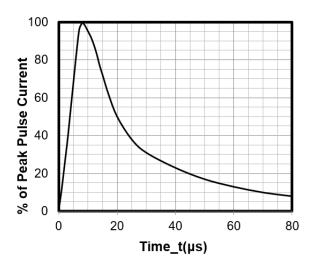
Typical Performance Characteristics (TA=25°C unless otherwise Specified)



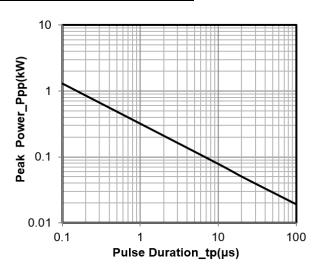
Junction Capacitance vs. Reverse Voltage



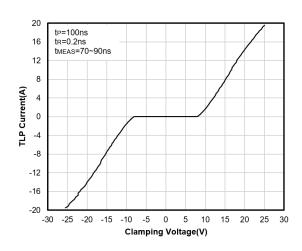
Clamping Voltage vs. Peak Pulse Current



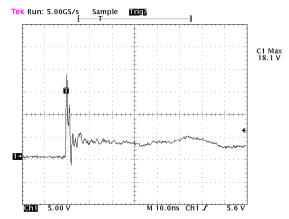
8 X 20µs Pulse Waveform



Peak Pulse Power vs. Pulse Time



TLP Curve



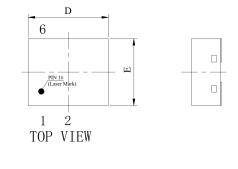
Note: Data is taken with a 10x attenuator

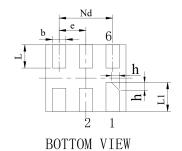
ESD Clamping Voltage

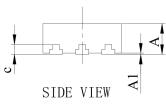
8 kV Contact per IEC61000-4-2



DFN1210-6 Package Outline Drawing







	DIMENSIONS					
SYM	MILLIMETERS					
·	MIN NOM		MAX			
Α	0.40	0.45	0.50			
A1	0	0.02	0.05			
b	0.15	0.20 0.25				
С	0.152REF					
D	1.15	1.20	1.25			
е	0.40BSC					
Nd	0.80BSC					
Е	0.95	1.00	1.05			
L	0.25	0.35	0.45			
L1	0.338	0.438	0.538			
h	0.07	0.12	0.17			

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