

SIT501

Photo Interrupter

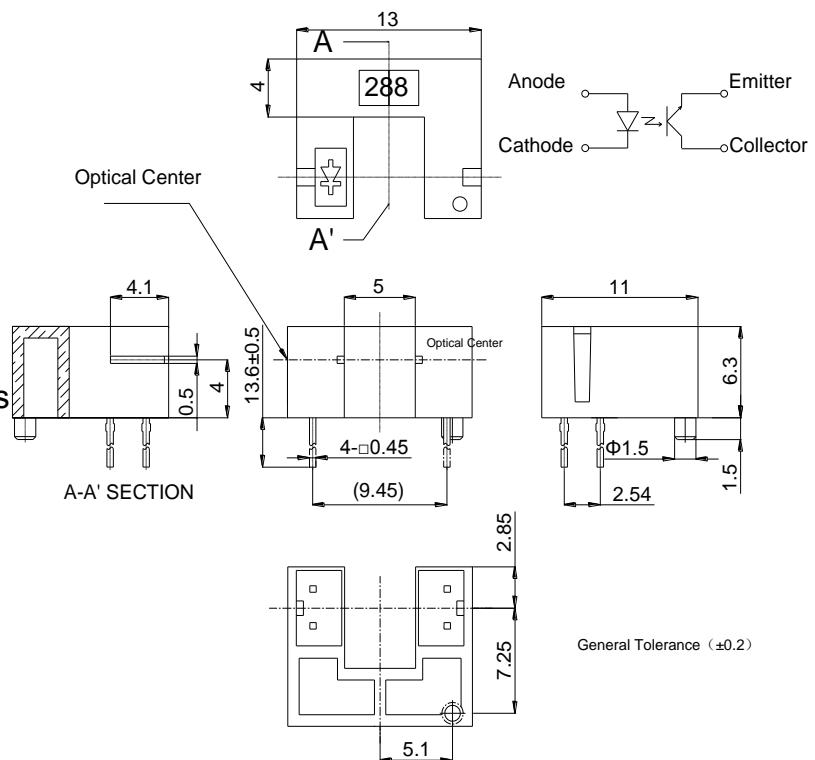
Dimensions

(unit: mm)

The SIT501 is a photointerrupter, high-performance standard type, combines high-output GaAs IRED with high sensitive phototransistor.

Features

- PCB direct mount type
- GAP:5.0mm
- With the installation positioning boss
- Horizontal slit
- RoHS Compliance



Applications

- FAX
- Mouses
- Rotary encoders

Maximum Ratings

(Ta=25°C)

Item		Symbol	Rating	Unit
Input	Power dissipation	P _D	100	mW
	Forward current	I _F	60	mA
	Reverse voltage	V _R	5	V
Output	Collector power dissipation	P _C	100	mW
	Collector current	I _C	40	mA
	Collector-Emiter voltage	V _{CEO}	30	V
	Emiter-Collector voltage	V _{ECO}	5	V
Operating temperature ^{*1}		To Pr.	-20 ~ +85	°C
Storage temperature ^{*1}		Tstg.	-30 ~ +85	°C
Soldering temperature ^{*2}		Tsol.	260	°C

*1. No icebond or dew

*2. The soldering should be 1.2mm away from bottom of the holder t=within 3s

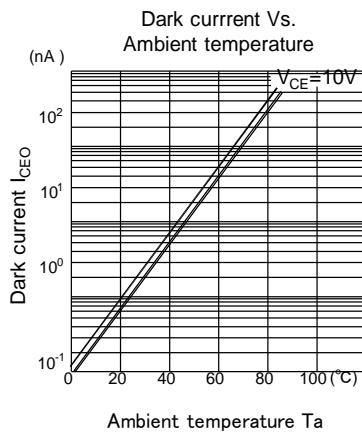
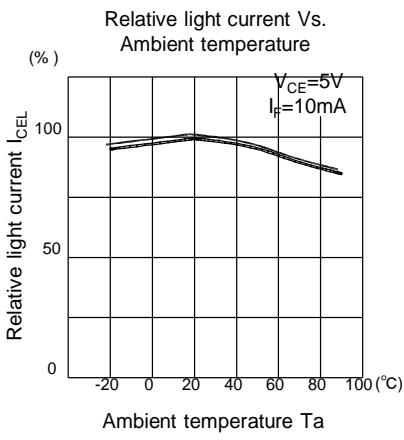
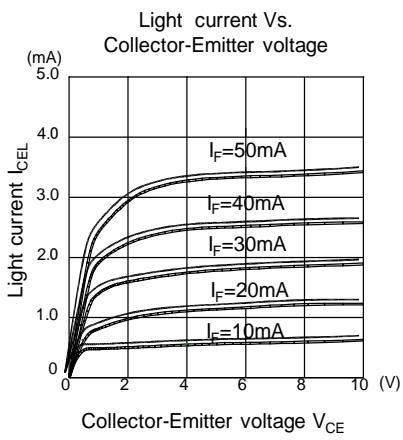
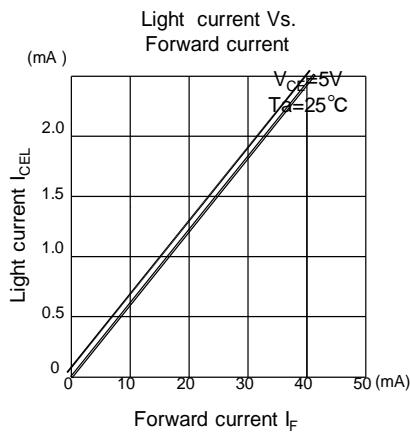
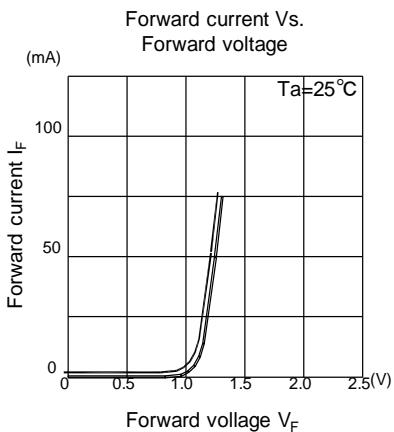
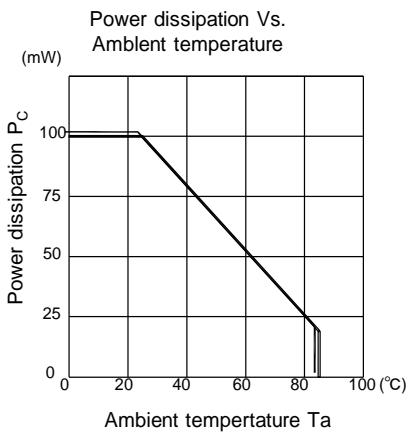
Photo Interrupter

SIT501

Elector-Optical Characteristics

(Ta=25°C)

Item		Symbol	Conditions	Min.	Typ.	Max.	Unit
Input	Forward voltage	V _F	I _F =20mA	-	1.2	1.4	V
	Reverse current	I _R	V _R =5V	-	-	10	μA
	Peak wavelength	λ _P	I _F =20mA	-	940	-	nm
Output	Dark current	I _{CEO}	V _{CE} =10V E _V =0 lx	-	1	100	nA
	Peak SENS.Wavelength	λ _P	-	-	880	-	nm
Transfer characteristics	Light current	I _{CEL}	I _F =20mA, V _{CE} =5V, Non-shading	0.2	-	10	mA
	Leak current	I _{CEOD}	I _F =20mA, V _{CE} =5V, Shading	-	0.5	10	μA
	C-E sat.voltage	V _{CE(sat)}	I _F =20mA I _C =0.1mA	-	0.15	0.4	V
	Rise time	tr	I _C =2mA V _{CC} =5V R _L =100 Ω	-	4	-	μs
	Fall time	tf		-	5	-	μs



The contents of this data sheet are subject to change without advance notice for the purpose of improvement. When using this product, would you please refer to the latest specifications.

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SIT501

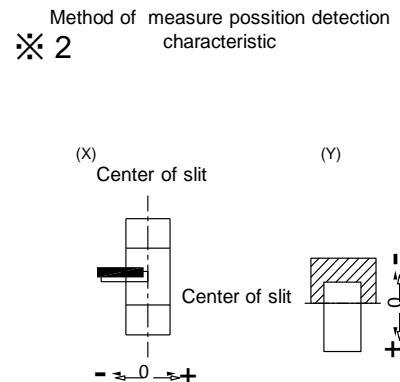
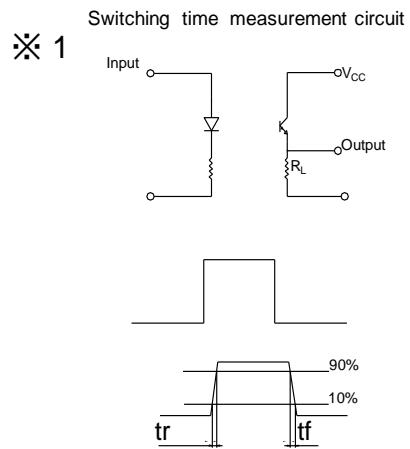
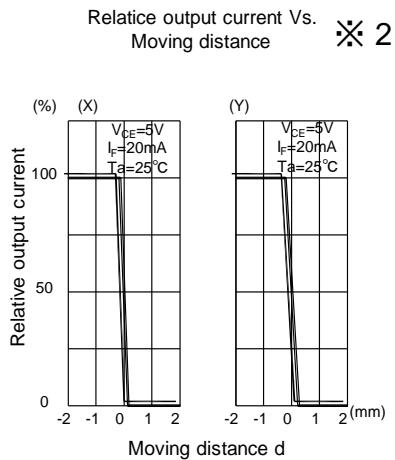
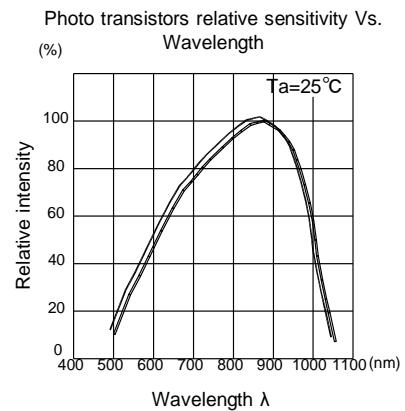
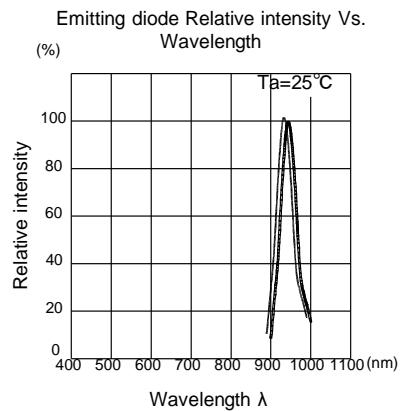
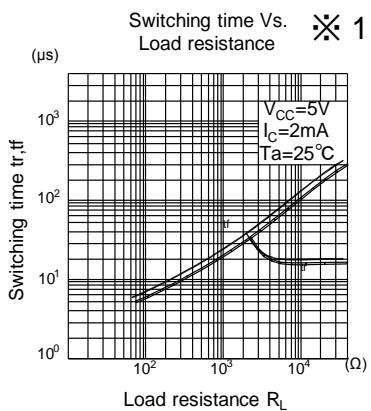
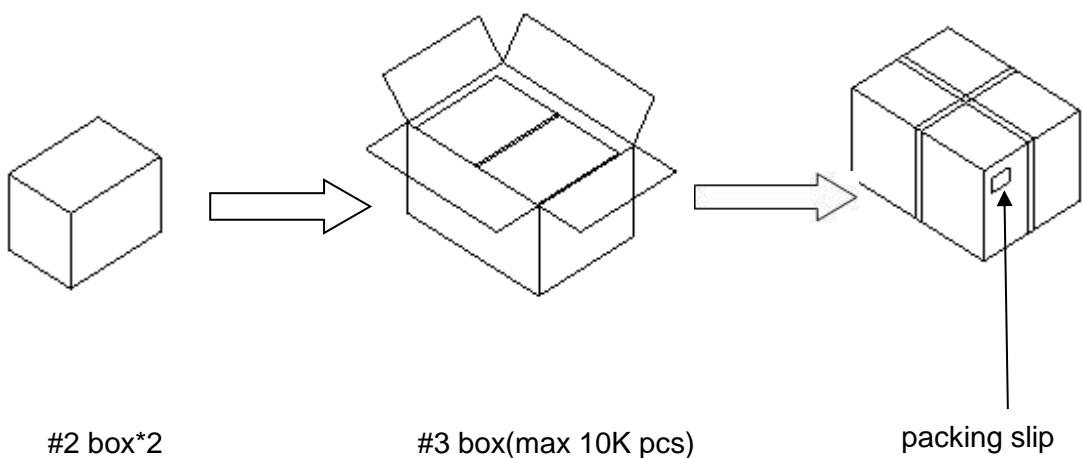
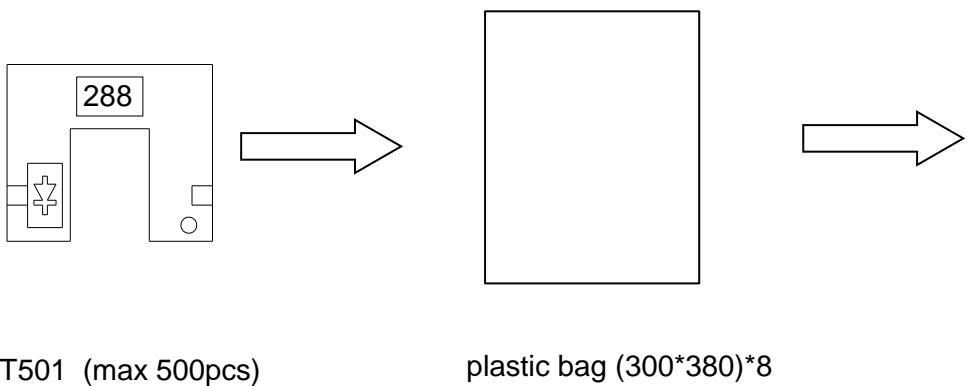


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SIT501

Packing Specification

1. Fixed quantity (max 500pcs) of the products are packed into plastic bag(300*380)
2. Seven bags of the products are put into #2 box
3. Two #2 boxes are put into #3 box(max 8000pcs)
4. Packing slip is pasted on #3 box.



SIT501

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Unit:mm

DIMENSIONS

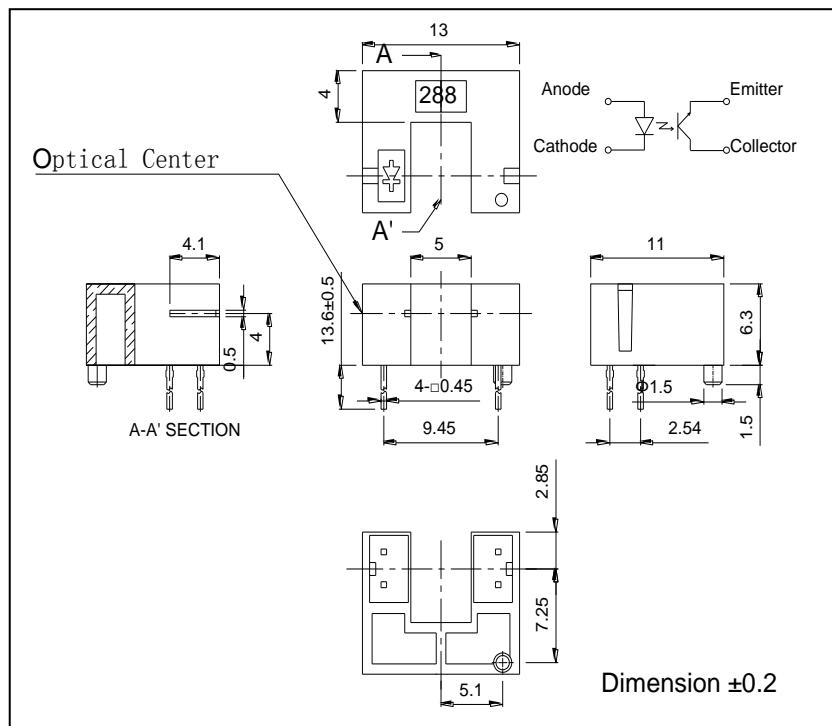
The SIT501 is a photointerrupter high-performance standard type, combines high-output GaAs IRED with high sensitive phototransistor.

Features

- PWB direct mount type
- GAP:5.0mm
- With the installation positioning boss
- Horizontal slit
- RoHS Compliance

Application

- FAX
- Mouses
- Rotary encoders

**MAXIMUM RATINGS**

Item	Symbol	Ratings	Unit
Input	P _D	100	mW
	I _F	60	mA
	V _R	5	V
Output	P _C	100	mW
	I _C	40	mA
	V _{CEO}	30	V
	V _{ECO}	5	V
Operating temperature * ¹	T _{opr}	-20~+85	°C
Storage temperature * ¹	T _{stg}	-30~+85	°C
Soldering temperature * ²	T _{sol}	260	°C

* 1.NO icebound or dew

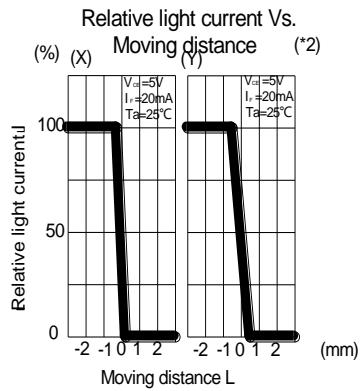
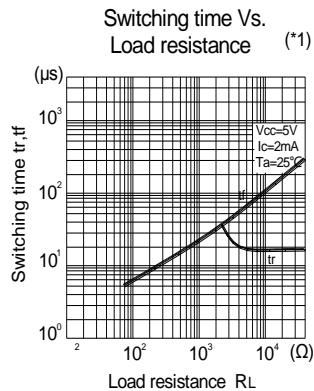
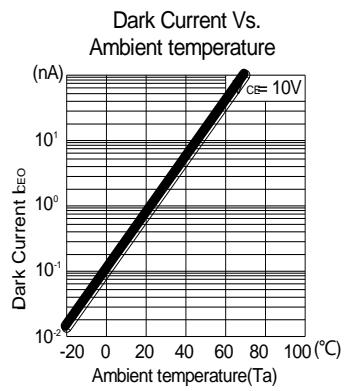
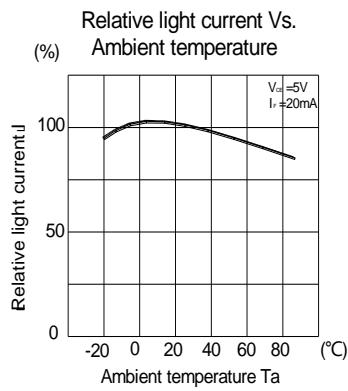
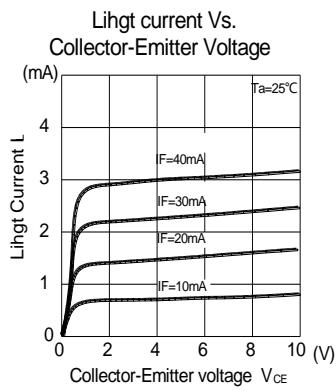
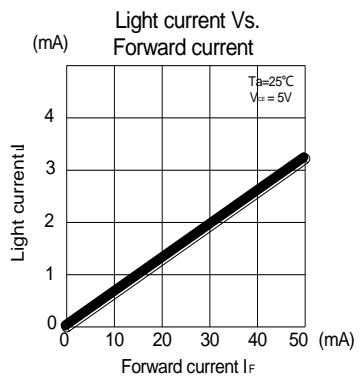
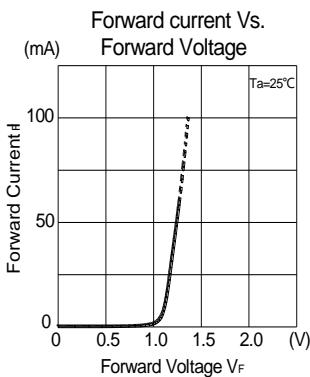
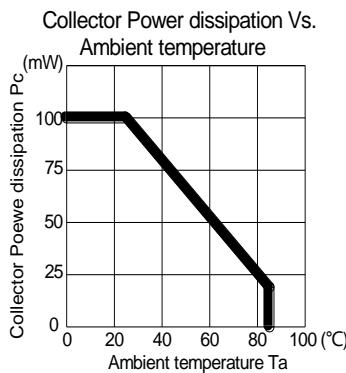
* 2.For Max 5 seconds at the position of 1mm from the resin edge

Elector-Optical Characteristics

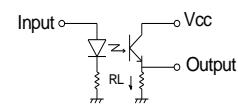
Item	Symbol	Conditions	Min	Typ	Max	Unit
Input	V _F	I _F =20mA	-	1.2	1.4	V
	I _R	V _R =5V	-	-	10	µA
	λ _P	I _F =20mA	-	940	-	nm
Output	I _{CEO}	V _{CE} =10V, E _V =0lx	-	1	100	nA
	λ _P	-	-	880	-	nm
Transfer characteristics	I _C	I _F =20mA, V _{CE} =5V, 入光状态 (Non-shading)	0.2	-	10	mA
	I _{CEOD}	I _F =20mA, V _{CE} =5V, 遮光状态 (Shading)	-	0.5	10	µA
	V _{CE} (sat)	I _F =20mA, I _C =0.1mA	-	0.15	0.4	V
	tr	I _C =2mA, V _{CC} =5V, R _L =100Ω	-	4	-	µs
	tf	I _C =2mA, V _{CC} =5V, R _L =100Ω	-	5	-	µs

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SIT501



*1 Switching time measurement circuit



Input

Output

*1 Switching time measurement circuit

(X) Center of slit (Y)

(+) (-)

0

(+) (-)

0

(+) (-)

0

(+) (-)

0

(+) (-)

0

(+) (-)

0

(+) (-)

0

(+) (-)

0

(+) (-)

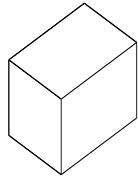
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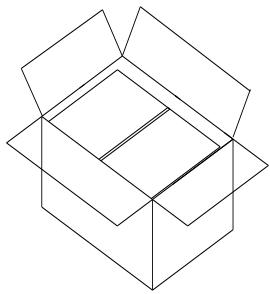
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Packing Specification

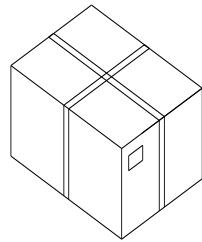
1. Fixed quantity (Max 500pcs) of the products are packed into plastic bags
2. Ten bags of the products are put into #2 Box (Max 5000pcs)
3. Two #2 Boxes are put into #3 Boxes
4. Paste packing slit



2个#2箱



1个#3箱



1个

